
H-82-0176

## Vistagraphic" $3000 /$

 Graphic 8"Series 8000COMPUTER GRAPHICS<br>DISPLAY SYSTEM

CPEPATION AND MAINTENANCEMANUAL

CALCOMP
A Sancers Graphes Company

The terms Vistagraphic 3000 and GRAPHIC 8 are interchangeable; they describe the same equipment. The term, GRAPHIC 8 is used throughout this manual.

CalComp/Sanders Associates, Inc., reserves the right to modify the products described in this manual and to make corrections or alterations to this manual at any time without notice.

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## RECORD OF CHANGES

| CHANGE NO. | DATE | TITLE OR BRIEF <br> DESCRIPTION | ENTEREO BY |
| :---: | :---: | :---: | :---: |
| 1 | OCt 82 | Correct errors in <br> original issue <br> NOV 82 | Add card cage <br> definitions <br> Feb 83 |

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The following are general safety precautions not related to any specific procedure and therefore do not appear elsewhere in this manual. These are recommended precautions that must be understood and applied during installation or maintenance of the terminal controller.

## AVOID LIVE CIRCUITS

Observe all safety regulations at all times. Do not replace components in the terminal controller power panel assembly with power applied.

## RESUSCITATION

When working with or near high voltages, be familiar with modern resuscitation methods.

WARNING

Primary power ( 100 Vac to 240 Vac ) is present at the power panel assembly. Line voltage of 115 Vac is present at the power supply. Always turn off teminal controller and pull power plug before moving any cabinet- or chassis-. mounted component.

TERMINAL CONTROLLER PROTECTION
Circuit card assemblies in the terminal controller can be damaged by transient surges.

## CAUTION

Always turn off terminal controller before removing or installing any circuit card.

SPECIAL HANDLING FOR MOS DEVICES
MOS devices are subject to damage caused by static charges. Assemblies that contain MOS devices are mapping memories and timing module. When not installed in the card cage, these assemblies should be stored in black Velostat bags with the MOS warning statement printed on the outside of the bag.

## CAUTION

Always handle these cards only by the card extractors or by the edges of the connector. Avoid touching the card components or the printed circuit.

## SPECIAL HANDLING EOR UV EPROMS

UV EPROMs are subject to damage by ultra violet radiation (including sunlight and some fluorescent lamps). Do not remove the paper cover from the sensitive area of the device. When not installed in the card cage, store the card assemblies in light-proof bags. Card assemblies that contain UV EPROMs are the digital graphic controller and expansion module.

## WARNING

This equipment generates, uses, and can radiate radio frequency energy; and if not installed and used in accordance with the instructions manual, may cause interference to radio communications. It has been tested and found to comply with the limits for a Class A computing device pursuant to Subpart $J$ of Part 15 of FCC Rules, which are designed to provide reasonable protection against such interference when operated in a commercial environment. Operation of this equipment in a residential area is likeIy to cause interference, in which case the user at his own expense will be required to take whatever measures may be required to correct the interference.

## NOTE

The GRAPHIC 8 was tested and found to comply with the limits for a Class A computing device with the terminal controller installed in a model 7895 equipment cabinet. If the user does not procure the model 7895 equipment cabinet, the user is responsible for proviaing an installation with comparable shielding qualities.

SECTION 1
GENERAL INFORMATION

## 1.1 <br> INTRODUCTION

The Sanders Associates, Inc. GRAPHIC $8^{\prime \prime}$ is a high-performance, intelligent computer graphics terminal system incorporating refreshed raster display technology. It is designed to interface a host computer and to support operator CRT display monitor stations configured with interactive devices such as keyboards, trackballs, forcesticks, and data tablets. It can also produce permanent hard copy records of displayed data.

The GRAPHIC 8 features a dynamic display update via a double refresh buffer memory technique, and supports up to four CRT display monitors. Resolutions of $512 \times 512,640 \times 480,1024 \times 768$ (interlaced) or $1024 \times 1024$ (interlaced) are available. Both color and monochrome versions are offered with up to 8 bits per pixel to provide as many as 256 simultaneous colors or monochrome intensities (or 128 plus blink).

The GRAPHIC 8 display processor is a general purpose digital computer with a set of over 400 instructions that controls a variety of functions to reduce the loading on the host computer. In combination with the host computer, the GRAPHIC 8 system permits the user to display digital data in a visual format on the CRT display monitor and to interact with the displayed image by means of keyboards, forcesticks, trackballs, data tablets, and PHOTOPENs®. Its high performance and intelligence make it well suited to a variety of applications, such as CAD/CAM, simulation and training, command and control, cartography, and many others.

### 1.2 COMPONENT DESCRIPTION

The basic GRAPHIC 8 system (figure l-1) consists of a terminal controller (figure 1-2) and a monitor. The basic system can be expanded to include a wide variety of options and enhancements.
1.2.1 TERMINAL CONTROLLER. The GRAPHIC 8 terminal controller consists of a rack mountable card cage and a power supply. As shown in figure $1-3$, the cards are interconnected by a processor bus and a graphic bus. The size of controller selected is based on the four following major considerations:
"GRAPHIC 8 is a trademark of Sanders Associates, Inc. ©PHOTOPEN is a registered trademark of Sanders Associates, Inc.


Figure 1-1. Typical GRAPHIC 8 System Configurations


COLOR DISPLAY MONITOR AND KEYBOARD
Figure 1-3. GRAPHIC 8 Terminal Controller Organization


1. Color or monochrome
2. Number of simultaneous colors or intensities
3. Resolution of the display image
4. Number of display stations per controller

The basic GRAPHIC 8 terminal controller consists of a l7-slot card cage with integral power supply. Six of the slots are for the processor cards, one slot is for the digital graphics controller, one slot is for the timing module, and the remaining nine slots are for mapping memory and video controller cards.
 control panel is also available. Twelve of the slots are for the processor cards, one slot is for the digital graphic controller card, one slot is for the timing module, and the remaining eleven slots are for mapping memory and video controller cards.

The following paragraphs give a brief description of each type of circuit card available for use with the GRAPHIC 8 terminal controler.
1.2.1.1 Display Processor. The display processor card is a general purpose digital computer that runs the control program and acts as master control for all devices connected to the processor bus. It contains multiple high-speed general-purpose registers that can be used as accumulators, pointers, index registers, or auto-indexing pointers in auto-increment and auto-decrement modes. Functions performed by the display processor card include system initialization, interface handing, local data editing, and local generation of simple display images.

Instructions used for the display processor emulate the instruction set for the PDP-11/34 manufactured by Digital Equipment Corporation (DEC( ). They are fetched either from the control program in read-only memory on the ROM and status logic card or from the read/ write memory.

An 8-bit configuration switch register on the display processor is program readable from octal location 177774. The switches may be set to an octal value and read like any other memory location. The four least significant bit switches are usually set to represent port assignments for keyboard and position entry devices. A second switch on the display processor allows the circuit to operate at either a 400 -nanosecond or a 300 -nanosecond cycle time.
1.2.1.2 Read/Write Memory. The GRAPHIC 8 system can accommodate up to 256 K words ( 512 K bytes) of read/write memory. Two card configurations are available: 64 K words and 256 K words. Also available are
depopulated versions of these two configurations. The GRAPHIC 8 can thus be fitted with one or two of the 64 K word cards, or one 256 K word card.

Those devices in the card cage that have 18-bit address lines (such as the digital graphic controller) can directly access addresses up to 128 K , less certain addresses which are reserved for other functions. For example, 0 to 4 K is reserved for interrupts and vector trap addresses, while 24 K to 32 K is reserved for the operating program and device addresses.

The display processor also has an 18-bit address line, giving direct access to 128 K words, less the reserved locations. In addition, the display processor can use the page registers on the read/write memory cards to access addresses above 128 K and to access memory locations with the same addresses as the reserved locations.

Figure 1-4 shows how the GRAPHIC 8 system memory may be mapped into $4 \mathrm{~K}, 8 \mathrm{~K}$, or 16 K word blocks.
1.2.1.3 ROM and Status Logic. The ROM and status logic card contains the read-only memory in which the program used to control the display processor is stored. Also contained on the card are display status and interrupt logic circuits plus a serial interface port to which a teletypewriter may be connected for diagnostic purposes. Like read/ write memory, read-only memory may be accessed to retrieve either 16 -bit words or individual 8 -bit bytes.

The standard read-only memory provided on the ROM and status logic card contains the Graphics Control Program (GCP+) in firmware. GCP+ occupies approximately 7.5 K words of read-only memory. GCP+ handles communications with the host computer, controls data entry devices, and performs built-in test routines. These functions reduce the software requirements for the host computer. Optionally available firmware further reduces the software requirements for the host computer. However, these programs are substantially different from GCP+.
1.2.1.4 Multiport Serial Interface. The multiport serial interface card contains four serial interface ports that operate in a serial asynchronous mode using RS-232C or TTL voltage levels with standard transmission rates up to 9600 baud. Additionally, the first port can be operated as a full RS-232C asynchronous interface at transmission rates greater than 9600 baud. For GCP applications, the maximum transmission rate supported is 9600 baud. Normally, the host computer is connected to the first port, which is compatible with the standard communication and terminal interfaces supplied by most computer manufacturers. The remaining three ports on the card are used for peripheral devices.
1.2.1.5 High-Speed Serial Interface. An optional, high-speed serial interface, designed for bit stream-oriented packet switching networks, is also available for the GRAPHIC 8. The high-speed serial interface supports the Consultative Committee for, International Telephony and


Figure 1-4. GRAPHIC 8 System Memory Map (Sheet 1 of 3)

| $\begin{aligned} & \text { OCTAL } \\ & \text { PAGE } \end{aligned}$ | EFFECTIVE ADDRESSES |
| :---: | :---: |
| 0 | MEMORY BANK 1 |
|  | $\begin{aligned} & \text { RESERVED } \\ & \text { MAP AREA } 1 \end{aligned} \quad\left\{\begin{array}{l} 000000: 017776 \\ 020000: 037776 \end{array}\right.$ |
| 1 | MAP AREA $2 \quad\left\{\begin{array}{l}040000: 057776 \\ 060000: 077776\end{array}\right.$ |
| 2 | 100000:117776 |
|  | 120000:137776 |
| 3 | GCP (ROM) 140000:157776 |
|  | DEVICE ADDRESSES 160000:177776 |
| 4 | 200000:217776 |
|  | 220000:237776 |
| 5 | 240000:257776 |
|  | 260000:277776 |
| 6 | 300000:317776 |
|  | 320000:337776 |
| 7 | $\begin{aligned} & 340000: 357776 \\ & 360000: 377776 \\ & \hline \end{aligned}$ |
| 10 | MEMORY 3ANK 2 |
|  | $\begin{aligned} & 400000: 417776 \\ & 420000: 437776 \\ & \hline \end{aligned}$ |
| 11 | 440000:457776 |
|  | 460000:477776 |
| 12 | $500000: 517776$ |
|  | 520000:537776 |
| 13 | 540000:557776 |
|  | 560000:577776 |
| 14 | $620000: 637776$ |
| 15 | 640000:657776 |
|  | 660000:677776 |
| 16 | 700000:717776 |
|  | 720000:737776 |
| 17 | 740000:757776 |
|  | 760000:777776 |
| MEMORY BANX 3 |  |
| 20 | $\begin{aligned} & 000000: 017776 \\ & 020000: 037776 \end{aligned}$ |
| 21 | $\frac{020000: 037776}{040000: 057776}$ |
|  | $060000: 077776$ |
| 22 | 100000:117776 |
|  | 120000:137776 |
| 23 | 140000:157776 |
|  | 160000:177776 |
| 24 | 200000:217776 |
|  | 220000:237776 |
| 25 | 240000:257776 |
|  | 260000:277776 |
| 26 | 300000:317776 |
|  | 320000:337776 |
| 27 | 340000:357776 |
|  | 360000:377776 |
| MEMORY BANK 4 |  |
| 30 | 400000:417776 |
|  | 420000:437776 |
| 31 | 440000:457776 |
|  | 460000:477776 |
| 32 | 500000:517776 |
|  | 520000:537776 |
| 33 | $\begin{aligned} & 540000: 557776 \\ & 560000: 577776 \\ & \hline \end{aligned}$ |
| 34 | 600000:617776 |
|  | 620000:637776 |
| 35 | 640000:657776 |
|  | 660000:677776 |
| 36 | $700000: 717776$ |
|  | 720000:737776 |
| 37 | 740000:757776 |
|  | 760000:777776 |



Figure 1-4. GRAPHIC 8 System Memory Map (Sheet 2 of 3)


## 16K WORDS MAPPING

Page Regiscers:

16X word mapping is NOT supported by Sanders Graphics Control Program (GCP).

A - Not used
B - Not used
C - address $172346_{8}$

2 K WORDS
Page Register Format:
(16K Word Octal Page Number)
$x=$ Don't Care
$64 K$ WORDS


6,

## NOTE

128K WORDS

1928 WORDS

Figure 1-4. GRAPHIC 8 System Memory Map (Sheet 3 of 3)

Telegraphy (CCITT) X. 25 protocol and operates in full duplex at up to 56 kilobaud.

The high-speed serial interface uses two cascaded, 4-bit microprocessor slices. In addition, a large scale integrated circuit (LSI) data link controller on the interface performs full cyclic redundancy checking (CRC) and zero insertion and deletion. Microprogram control storage. for the interface is provided by on-board ROM. However, the high-speed serial interface also requires the use of the EPROM expansion module described in paragraph 1.2.1.13. The EPROM expansion module stores the additional software needed for the GRAPHIC 8 to handle input/output via the high-speed serial interface.

The electrical characteristics of the high-speed interface conform to EIA standard RS449.
1.2.1.6 Parallel Interface. Use of an optional GRAPHIC 8 parallel interface allows high-speed host-to-GRAPHIC 8 communications, with handshaking. The various parallel interface cards available are intended for applications where the GRAPHIC 8 is located in proximity to the host. All parallel interface signals are TrL-compatible. Under program control, the interface operates in either an interrupt driven or a DMA mode. In the latter mode, the interface operates at speeds up to 500,000 16-bit words/second. If a parallel interface card is installed in the terminal controller, GCP+ assumes that it is connected to the host computer. Therefore, if serial communication with the host computer is desired, a parallel interface card cannot be connected to the processor bus.

## NOTE

Normally, if a parallel interface port is used, a single parallel interface card (for the host computer) is installed in the terminal controller. For special applications, however, two parallel interface caras may be installed, but are not supported by the standard Graphics Control Program.
1.2.1.7 Digital Graphic Controller. The digital graphic controller is a microprocessor with more than 50 instructions committed to ROM. It retrieves display update instructions from read/write memory, generates vectors, characters, conics, point plots, and fills, and stores these in mapping memory in raster-scan format. The complete series of sequential instructions that defines any particular display image is referred to as a refresh file.

The digital graphic controller may be considered as a device on the processor bus of the terminal controller. It contains its own set of registers that maintain instruction address, control fetch operations, and perform any branching that may be specified by non-graphic instructions. It also calculates relative data when required, loads data into appropriate registers, and initiates execution of refresh file instructions.


#### Abstract

Status bits of the digital graphic controller are maintained by circuits on the ROM and status logic card. These bits plus the graphic controller registers are accessible to the display processor which maintains control over the entire terminal controller.


The digital graphic controller also performs character generation. The basic set of characters supplied is a standard set of 96 ASCII characters. When the ASCII code corresponding to the desired character is applied to the read-only memory, the character is drawn at the position specified by the current position for $X$ and $Y$.

As determined by instructions from the digital controller, characters of three different sizes can be generated. Typically, the digital graphic controller generates a 7 x 9 pixel character set or a $5 \times 7$ pixel character set. Characters may also be rotated 90 degrees counterclockwise to accommodate vertical writing requirements. Both normal and rotated characters can be made to blink.

Read-only memory for six groups of 16 characters can be added to provide a total of 192 standard and special characters that can be produced by the GRAPHIC 8.
1.2.1.8 Mapping Memory. The mapping memory contains pixel data in a format which allows display refresh in a raster scan mode. The mapping memory may be configured for various resolutions up to 1024 x 1024 (see figure $1-5$ ) and for interlace or non-interlace refresh. A single memory board can be supplied with a capacity of over four million bits. Up to eight bits can be combined per pixel to provide 256 possible colors or intensity levels.

A dual mapping memory configuration provides high-speed dynamic update of data. The dual memory concept allows the hardware to clear and update one memory while the second memory is refreshing the display. When the next update occurs, the roles of the two memories are reversed so that the previously updated memory now becomes the refresh memory.

Additional mapping memory cards may be connected in parallel, as needed, to provide the required resolution or number of bits per pixel.
1.2.1.9 Video Controller. The video controller obtains data from the mapping memory and formats it for presentation on the display monitor(s). Outputs are provided as either RGB color or monochrome and as composite video.

External video may be accepted by the video controller and logically ORed with internally-generated video. A single video controller can accommodate up to eight bits per pixel.

The video controller generates one non-destructive, full screen, crosshair cursor and contains the cursor address registers which are accessible to the user. It controls the split screen function which


Figure 1-5. Addressable Vs. Displayable Areas for Four Screen Resolutions
allows the user to divide the display face into two or three variableheight horizontal bands and fill these bands with data from anywhere in addressable mapping memory. This feature allows the user to simultaneously view up to three separate areas of mapping memory which are not necessarily contiguous.

The video controller contains a 256 x 8-bit word RAM look-uptable (LUT) which allows pseudo-color or pseudo-gray level transformation to be made.
1.2.1.10 Timing Module. The timing module generates all displayrelated timing signals as well as the necessary synchronization signals for the monitors. On-board switches allow selection for compatible operation with the possible resolutions and refresh frequencies.
1.2.1.11 2D Coordinate Converter. The optional 2 D coordinate converter permits programed rotation and translation of the image on the CRT display. Rotation commands cause 0 degrees to 360 degrees rotation about any point in the display area. Rotation may be either clockwise or counterclockwise. Translation commands cause the image to move anywhere in the display area. Rotation and translation can be used independently, or translation can follow rotation. Rotation and translation affect only the selected elements of the display presentation, independent of the rest of the display.
1.2.1.12 3D Coordinate Converter. The optional 3D coordinate converter converts a Sanders graphic display into a three dimensional display capable of independent dynamic manipulation of objects in apparent space. Among the functions provided by the 3D coordinate converter are translation, scaling, rotation, windowing, independent displáy coordinate mapping, perspective, and zooming with perspective.

The perspective feature is especially useful for realistic viewing of an object. Utilizing perspective, the location of the viewer is defined relative to the image space, and all lines and objects within the image space are then viewed at the proper perspective for that location. The view may be completely orthographic if the viewer does not wish to use the perspective feature.

Objects can be defined within a $64 \mathrm{~K}(\mathrm{X}), 64 \mathrm{~K}(\mathrm{Y})$, by $32 \mathrm{~K}(\mathrm{Z})$ image space and presented on $a \operatorname{lK}$ by $1 K$ screen or any portion thereof. Translations can be made within the limits of the image space and scaling range is 64 to 1 . Rotation can be provided about any axis.

3D windowing, in conjunction with independent screen coordinate mapping, allows the presentation of any data within a software definable $X, Y, Z$ image space to be presented on the full screen or any portion of the screen. Zooming is accommodated by scaling and changing the user's apparent perspective viewpoint.

Alphanumeric data can be moved about the screen with vector defined data without scaling and rotation.

The 3D coordinate converter provides for both homogeneous and non-homogeneous matrix operation. Also, transformations of 2 D images can be accomplished including translation, rotation, scaling, and windowing.
1.2.1.13 EPROM Expansion Module. As options are added to the GRAPHIC 8, the additional software required to handle the options is stored on the EPROM expansion module.

The expansion module may contain up to $32 \mathrm{~K} \quad 16$-bit words of nonvolatile read-only memory (EPROMs). The data may be loaded from the expansion module automatically by pressing the SYSTEM button or when so instructed by the host, depending on the options stored.
1.2.2 INPUT DEVICES. Optional data input devices for the GRAPHIC 8 give the operator two-way interaction with the display and processing system. Input devices available include various keyboards, a trackball, a forcestick, a data tablet, and a PHOTOPEN. The standard control program in firmware can support up to eight keyboards, eight position entry devices (trackball, forcestick, or data tablet), or two PHOTOPENs. In addition, a teletypewriter or paper tape reader can be connected to the GRAPHIC 8 for the input of maintenance data.
1.2.2.1 Reyboards. Standard keyboards available for the GRAPHIC 8 are the Model 5783 and Model 5784 keyboards. The keyboards contain a main block of alphanumeric keys plus a matrix and a row of function keys.

The Model 5783 keyboard offers an alphanumeric block of 58 keys. These keys generate standard seven-bit ASCII codes with an eighth (MSB) bit always set to 1. The alphabetic keys generate both upper and lower case codes. A four-by-four matrix of function keys is located to the right of the alphanumeric block and a row of 16 function keys is located immediately above the alphanumeric block. Each function key generates a single eight-bit octal code from 000 to 037.

An added feature of the Model 5784 keyboard is that each function key contains an LED that can be lighted or turned off as required under program control. The Model 5784 also has provisions for additional keys to the basic board. These keys are for future expansion and are located on both sides of the space bar.

The keyboards operate at a rate of 9600 baud and interface to the terminal controller via ports on the multiport serial interface card.
1.2.2.2 Trackball, Forcestick, and Data Tablet. The trackball, forcestick, and data tablet are referred to as peds (position entry devices). These devices are used as determined by program control to move a cursor and/or data displayed on the CRT screen. Movement initiated by the trackball is proportional to the speed and direction in which the trackball is rolled. Movement initiated by the forcestick is proportional to the direction and force with which the forcestick is deflected. Movement initiated by a data tablet is
proportional to the speed and direction in which the data tablet pen is moved along the data tablet surface. PEDs are connected to the system via ports on the multiport serial interface card(s) in the terminal controller.
1.2.2.3 PHOTOPEN. The PHOTOPEN is a small hand-held device that detects light from data displayed on the CRT of a display monitor. Detected light is converted into an electrical impulse to identify the specific data at which the pHOTOPEN is pointed. The excellent resolving capability of the pHOTOPEN enables individual characters and even displayed points of light to be distinguished.
-
A switch in the PHOTOPEN is actuated when the PHOTOPEN is pressed against the CRT screen. Actuation of this switch causes the data sensed by the PHOTOPEN to be processed as determined by program control.
1.2.2.4 Maintenance Data Input Devices. A teletypewriter and/or a paper tape reader can be connected to the GRAPHIC 8 to input data for maintenance purposes. The teletypewriter is normally connected to a port on the ROM and status card in the terminal controller while the paper tape reader is connected to one of the ports on a multiport serial interface card. The teletypewriter serves basically as a troubleshooting aid. The paper tape reader is used to load special user or diagnostic programs into the GRAPHIC 8.
1.2.3 OUTPUT DEVICES. The standard output device for the GRAPHIC 8 is the CRT display monitor. A hardcopy unit is available as an optional device. Using the same signals that go to a steandard display monitor, the hardcopy unit can produce a duplicate on paper of any static image displayed on the CRT of the display monitor. Operation. of the hardcopy unit is controlled manually.

An optional hardcopy multiplex switch is available. The multiplex switch is capable of interfacing up to four GRAPHIC 8 terminal controllers to a single hardcopy unit.
1.2.3.1 Display Monitors. The GRAPHIC 8 offers the user a choice of configuration of various CRT monitors (both monochrome and color) to provide the right monitor for the intended application.

Positions on the screen are specified in terms of a matrix containing 2048 coordinates in the $x$ dimension and 2048 coordinates in the $Y$ dimension. Two's complement notation is used to designate the coordinates with location 0 , 0 being defined as the center of the screen. Of the 2048 by 2048 addressable locations, the displayable area comprises the field of coordinates centered about the middle of the screen. Refer to figure l-5 for different screen resolutions.
1.2.3.2 Hardcopy. Both monochrome and color hardcopy devices are available for use with the GRAPHIC 8.

## TERMINAL CONTROLIER PHYSICAL DESCRIPTION

The terminal controller comprises a card cage, a power supply, two fans, a control panel, and a protective cover mounted on the back.

The teminal controller may be mounted either in a standard 19-inch equipment rack, an optional cabinet, or an optional desk assembly.

The circuit cards are inserted into the card cage from the front of the teminal controller, and plug into a wire-wrapped backplane. The blower fans, located beneath the card cage, draw air from the bottom of the unit and discharge the air through the top.

The teminal controller can operate with 100 V - 120 V or 220 V 240 V ac input power. An input power control panel (located in a lower front of the equipment cabinet) contains a fuse, a power receptacle, and a removable configuration plug. The configuration plug must be wired for the proper voltages. The power panel contains two outlets. One outlet is live when the circuit breaker is ON. The other outlet is live when the circuit breaker is on and a control signal is applied through a connector ( P 2 ) on the power panel.

Both a standard, 17-slot card cage and an optional, 25-slot card cage are available. The following paragraphs describe each of these teminal controller configurations in more detail.
1.3.1 STANDARD CARD CAGE. The card cage supplied for a particular installation is selected as a function of the number of monitors to be driven and the number of cards on the processor bus. See figure $1-6$ and table 1-1.

Table 1-1. Standard Card Cages

| CARD CAGE |  |  | BACRPLANE |
| :--- | :--- | ---: | :---: |
| PART NO. | CHARACTERISTICS | SEE FIGURE | DGM |

5977184 G 1 Single monitor, 4 slots available for read/write memory and processor bus options

5977184 G 2
Three monitors, 4 slots avail- B, figure 1-6
5977296 able for read/write memory and processor bus options
$5977184 \mathrm{G3}$
Four monitors, 4 slots availC, figure 1-6

5802978
able for read/write memory
and processor bus options
$5809900 G 1$
Two monitors, 7 slots avail-
D, figure 1-6
5809899
able for read/write memory and processor bus options


Table 1-1. Standard Cara Cages (Cont)

| CARD CAGE |  |  | BACKRLANE |
| :--- | :--- | :---: | :---: |
| PART NO. | CHARACTERISTICS | SEE FIGURE | DGM |

5809900 G 2 Single monitor, 7 slots avail- E, figure 1-6 5809850 able for read/write memory and processor bus options

The card cage assembly number appears at the rear of the card cage, below the power supply (see F, figure 1-6).

On the standard card cage, the terminal controller control panel covers the front of the unit and must be removed for access.

Table l-1A lists the physical characteristics of the standard card cage and circuit cards.
1.3.2 EXTENDED CARD CAGE. The extended card cage typically contains a minimum of eleven circuit cards. Additional cards may be inserted as required.

The low voltage power supply and controls and indicators for the extended card cage are mounted on a separate panel. This allows unobstructed access to the circuit cards, as needed for adjustment or troubleshooting.

Table 1-1A. Standard Card Cage Physical Characteristics

| TERMINAL CONTROLIER |  |
| :---: | :---: |
| Height | 10.5 inches ( 26.8 cm ) |
| Width | 19.0 inches ( 48.2 cm ) including mounting flanges |
| Depth | 16.0 inches ( 40.6 cm ) |
| Weight | 55 pounds ( 25 kg ) including circuit cards |
| CIRCUIT CARDS |  |
| Height | 12-3/8 inches ( 31.4 cm ) |
| Width | 7-3/4 inches ( 19.7 cm ) |

Figure $1-7$ shows the circuit card order assumed for a full complement of circuit cards.

Table l-2 lists the physical characteristics of the extended card cage and circuit cards.


H-82-0176-007

Figure 1-7. Extended Card Cage, Cara Locations (Typical) Function Keyboard

Table 1-2. Extended Card Cage Physical Characteristics


### 1.4 INPUT DEVICES PHYSICAL DESCRIPTION

A variety of input devices are available for use with GRAPHIC 8 systems. These include several keyboards, a trackball, a forcestick, a data tablet, and a PHOTOPEN. The following paragraphs describe each of these input devices in more detail.
1.4.1 KEYBOARD PHYSICAL DESCRIPTION. Three keyboard models are currently available for use with GRAPHIC 8 systems: models 5783, 5784, and 5789.
1.4.1.1 Model 5783/5784 Reyboards. Each model 5783 or 5784 keyboard is self contained in a cast aluminum housing, designed for desk top or similar use. Figure l-8 shows a typical keyboard.


Figure 1-8. Typical Model 5783/5784 Keyboard
The keyboard contains three separate key sets: a main block of 55 ASCII alphanumeric, symbol, and special purpose keys; a four-byfour matrix of 16 function keys to the right (the matrix keyboard); and a row of 16 function keys across the top (the function keyboard).

Both the model 5783 and the model 5784 keyboad have a lamp in the CAPS LOCK key. In addition, the model 5784 keyboard contains a lamp in each matrix key and each function key. Table l-3 lists the principal differences between different versions of the model 5783 and model 5784 keyboards.

Table 1-3. Principal Differences Between Keyboards

| MODEL | LOGIC TYPE | KEYBOARD | LOCAL MODE <br> CONTROL | AUDIBLE <br> ALARM | TMY <br> EMULATOR |
| :--- | :--- | :--- | :---: | :---: | :---: |
| 5783 | Discrete <br> RAM/ROM | Flat | NO | NO | NO |
| $5783 A$ | Microprocessor | Flat | Yes | No | Yes |
| 5784 | Microprocessor | Stepped | Yes | Yes | Yes |
| $5784 A$ | RAM/ROM | Microprocessor | Flat | No | No |
| $5784 B$ | Microprocessor | Stepped | Yes | Yos | No |

The keyboard interfaces with the terminal controller through one of the terminal controller's serial interface ports. An adapter cable is required between the keyboard cable and the serial interface port connector. No adapter is required when the keyboard cable goes to the system interconnect panel. Refer to section 3 for details. All power required to operate the keyboard is supplied by the terminal controller.

Table 1-4 lists the keyboard specifications. The model 5783 and model 5784 keyboards are commercial units built to Sanders' specifications. The units are supplied under manufacturer's warranty to Sanders and are not considered field maintainable.

Table 1-4. Model 5783/5784 Keyboard Specifications

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| Alphanumeric keys | 55, momentary action, of which 50 are upper and lower case |
| Matrix keys | 16, momentary action |
| Function keys | 16, momentary action |
| Power | $\begin{aligned} & +5 \mathrm{~V}, \pm 1 \%, \quad 750 \mathrm{~mA} \max \\ & +15 \mathrm{~V}, \pm 1 \%, 50 \mathrm{~mA} \max \\ & -15 \mathrm{~V}, \pm 1 \%, 50 \mathrm{~mA} \max \end{aligned}$ |
| Output levels | Logic low is 0.0 to +0.45 V Logic high is +2.45 to +5.25 V |
| Output signal characteristics | Serial, RS-232 compatible, negative true |
|  | - Rest $=$ low <br> - Logic 1 = low <br> - Code $=10$ bit (one start bit, eight data bits, one stop bit) |
| Maximum cable length (keyboara to terminal controller) | Model 5783-50 feet <br> Model 5784-100 feet with external <br> +5 V power supply |
| Operating temperature range | $+4^{\circ} \mathrm{C}$ to $+49^{\circ} \mathrm{C}\left(40^{\circ} \mathrm{F}\right.$ to $\left.120^{\circ} \mathrm{F}\right)$ |
| Storage temperature range | $-40^{\circ} \mathrm{C}$ to $+60^{\circ} \mathrm{C}\left(-40^{\circ} \mathrm{F}\right.$ to $\left.140^{\circ} \mathrm{F}\right)$ |
| Humidity | 5\% to 95\%, non-condensing |
| Storage pressure range | 483 mm Hg to $813 \mathrm{~mm} \mathrm{Hg} \mathrm{(19}$ to 32 inches Hg) |

Table 1-4. Model 5783/5784 Keyboard Specifications (cont)

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| Dimensions | 475 mm wide by 206 mm deep by 94 mm <br> high $(18.75$ by 8.1 by 3.7 inches $)$ <br> Weight |
|  | $3.2 \mathrm{~kg}(71 \mathrm{bs})$. |

1.4.1.2 Model 5789 Keyboard. The model 5789 alphanumeric, fixed function, programable function keyboard (AN/FF/PFK) is self contained in an aluminum housing, designed for desk top or similar use. Figure I-9 shows the AN/FF/PFK keyboard.


Figure 1-9. Model 5789 Keyboard
The keyboard contains three separate key sets: a main block of 75 ASCII alphanumeric, symbol, and fixed function keys; a four-by-four matrix of 16 programmable function keys to the right (the matrix keyboard); and a row of 20 programmable function keys across the top (the function keyboard). In addition, 12 of the keys in the top row of the main block are also programmable, making 48 programmable function keys in all.

Each key (except the SHIFT, ALT, and LOCK keys), when pressed, causes a lo-bit code to be generated and sent to the multiport serial interface in the terminal controller. The lo-bit code consists of a start bit, eight data bits, and a stop bit. The least significant bit of the eight data bits is sent first. the most significant bit last. The serial data has the following characteristics:

Logic $1=10 w$ (negative true)
Rest $=10 \mathrm{w}$
Data rate $=9600$ baud
The SHIFT, ALT, and LOCK keys modify the codes of the other keys. The ALT key must be held down while the other key is pressed to achieve the ALT function. The SHIFT keys may either be held down while the desired key is pressed, or the LOCK key may be pressed to lock the shift function. The LOCK key is an alternate action switch; pressing it once locks the function, pressing it again releases the function.

Table l-5 lists the key codes.
Table 1-5. Model 5789 Key Codes


Table 1-5. Model 5789 Rey Codes (Cont)

| $\begin{aligned} & \text { KEY } \\ & \text { NO. } \end{aligned}$ | UNSHIFTED |  | SEIFTED |  | ALTERNATE |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 18 | PF4 2 | 052 | PF42 | 052 |  |  |
| 19 | PF43 | 053 | PF43 | 053 |  |  |
| 20 | PF44 | 054 | PF44 | 054 |  |  |
| 21 | SCRN SEL (SCREEN SELECT) | 206 | SCRN SEL | 206 | (SPARE) | 217 |
| 22 | RESHOW | 221 | RESHOW | 221 | CLEAR | 201 |
| 23* | $\begin{aligned} & \text { (ACCENT } \\ & \text { GRAVE) } \end{aligned}$ | 340 | $\sim(T I L D E)$ | 376 | (SPARE) | 177 |
| 24* | 1 | 261 | 1 | 335 | PF1 | 001 |
| 25* | 2 | 262 | @ | 300 | PF2 | 002 |
| 26* | 3 | 263 | \# | 243 | PF3 | 003 |
| 27* | 4 | 264 | \$ | 244 | PF4 | 004 |
| 28* | 5 | 265 | \% | 245 | PF5 | 005 |
| 29* | 6 | 266 | $\square$ | 336 | PF6 | 006 |
| 30* | 7 | 267 | \& | 246 | PE7 | 007 |
| 31* | 8 | 270 | * | 252 | PF8 | 010 |
| 32* | 9 | 271 | 1 | 250 | PF9 | 011 |
| 33* | 0 | 260 | ) | 251 | PE10 | 012 |
| 34* | - (HYPHEN) | 255 | - (UNDERLINE) | 337 | PF11 | 013 |
| 35* | $=$ | 275 | $+$ | 253 | PF12 | 014 |
| $36 *$ | $\begin{aligned} & \text { SPACE) } \\ & \text { (BACK } \end{aligned}$ | 210 | - | 210 | (SPARE) | 071 |
| 37 | DUP | 222 | DUP | 222 | PAI | 202 |
| 38 | FIELD MARK | 223 | FIELD MARK | 223 | PA2 | 203 |
|  |  |  |  |  |  | - |

Table 1-5. Model 5789 Key Codes (Cont)

| $\begin{aligned} & \text { KEY } \\ & \text { NO. } \end{aligned}$ | UNSHIFTED |  | SHIFTED |  | ALTERNATE |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | SYMBOL | CODE | SYMBOL | CODE | SYMBOL | CODE |
| 39 | ALARM ACK | 220 | ALARM ACK | 220 | ENBL DSBL (ENABLE/ DISABLE ALARM) | 200 |
| 40 | GRAPH PICK | 224 | GRAPH PICK | 224 | ERASE INPUT | 204 |
| 41* | $\longrightarrow$ (TAB) | 211 | $\longrightarrow$ | 211 |  |  |
| 42* | q | 361 | Q | 321 |  |  |
| 43* | W | 367 | W | 327 |  |  |
| 44* | e | 345 | E | 305 |  |  |
| 45* | $r$ | 362 | R | 322 |  |  |
| 4 6* | t | 364 | T | 324 |  |  |
| 47* | $Y$ | 371 | Y | 331 |  |  |
| 48* | u | 365 | U | 325 |  |  |
| 49* | i | 351 | I | 311 |  |  |
| 50* | 0 | 357 | 0 | 317 |  |  |
| 51* | $p$ | 360 | p | 320 |  |  |
| $52 *$ | ¢ | 333 | ! | 241 |  |  |
| 53* |  | 334 |  | 374 |  |  |
| 54* | -(BACK TAB) | 225 | ${ }^{-}$ | 225 | HOME | 205 |
| 55 | INS MODE (INSERT CHARACTER) | 237 | INS MODE | 237 | (SPARE) | 171 |
| 56 | DEL (DELETE CHARACTER) | 377 | DEL | 377 | (SPARE) | 172 |
| 57 | ANK ZONE | 232 | ANK ZONE | 232 | MOVE | 212 |
| 58 | ERASE EOF | 234 | ERASE EOF | 234 | SWITCH OVER | 214 |
| 59 | LOCK | NONE | LOCR | NONE |  |  |

Table 1-5. Model 5789 Key Codes (Cont)

| $\begin{aligned} & \text { REY } \\ & \text { NO. } \end{aligned}$ | UNSAIFTED <br> SYMBOL | CODE | $\text { SYMBOL }{ }^{\text {SEII }}$ | CODE | ALTERNA SYMBOL | CODE |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 60* | a | 341 | A | 301 |  |  |
| $61 *$ | s | 363 | $s$ | 323 |  |  |
| 62* | d | 344 | D | 304 |  |  |
| $63^{*}$ | $\underline{E}$ | 346 | F | 306 |  |  |
| 64* | g | 347 | G | 307 |  |  |
| 65* | h | 350 | H | 310 |  |  |
| 66* | j | 352 | $J$ | 312 |  |  |
| 67* | $k$ | 353 | K | 313 |  |  |
| 68* | 1 | 354 | L | 314 |  |  |
| 69* | ; | 273 | : | 272 |  |  |
| 70* | , | 247 | " | 242 |  |  |
| 71* |  | 373 | $\}$ | 375 |  |  |
| 72* | (NEW LINE) | 215 |  | 215 |  |  |
| 73* | $\mid$ (CURSOR UP) | 236 | 1 | 236 | (SPARE) | 173 |
| 74* | (CURSOR DOWN) | 231 | 1 | 231 | (SPARE) | 174 |
| 75 | PRINT | 233 | COPY | 216 | LEFT FREZE | 213 |
| 76 | PFK ZONE | 227 | PFK ZONE | 227 | TEST | 207 |
| 77 | SEIFT | NONE | SHIFT | NONE |  |  |
| 78* | $<$ | 274 | > | 276 |  |  |
| 79* | $z$ | 372 | 2 | 332 |  |  |
| 80* | x | 370 | X | 330 |  |  |
| 81* | $c$ | 343 | C | 303 |  |  |
| 82* | v | 366 | V | 326 |  |  |
| 83* | b - | 342 | B | 302 |  |  |

Table 1-5. Model 5789 Key Codes (Cont)

| $\begin{aligned} & \text { REY } \\ & \text { NO. } \end{aligned}$ | SYMBOL UNSHI | CODE | ${ }^{\text {SYMBOL }}{ }^{\text {SHIE }}$ | CODE | ALTERNATE SYMBOL | CODE |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 84* | ก | 356 | N | 316 |  |  |
| 85* | m | 355 | M | 315 |  |  |
| 86* | , | 254 | , | 254 |  |  |
| 87* | - | 256 | - | 256 |  |  |
| 88* | / | 257 | ? | 277 |  |  |
| 89 | SHIFT | NONE | SHIFT | NONE |  |  |
| 90* | $\qquad$ (CURSOR LEFT) | 210 | $\underline{\square}$ | 210 | (SPARE) | 175 |
| 91* | $\overline{R I G H T}^{(\text {CURSOR }}$ | 226 | - | 226 | RIGAT FREEZE | 176 |
| 92 | RESET | 230 | RESET | 230 |  |  |
| 93* | SPACE BAR | 240 | SPACE BAR | 240 |  |  |
| 94 | ALT | NONE | ALT | NONE |  |  |
| 95 | ENTER | 235 | ENTER | 235 |  |  |
| 96 | PF13 | 015 | 7 | 267 |  |  |
| 97 | PF 14 | 016 | 8 | 270 |  |  |
| 98 | PF 15 | 017 | 9 | 271 |  |  |
| 99 | PF45 | 055 | (SPARE) | 170 |  |  |
| 100 | PF 16 | 020 | 4 | 264 |  |  |
| 101 | PF17 | 021 | 5 | 265 |  |  |
| 102 | PF18 | 022 | 6 | 266 |  |  |
| 103 | PF 46 | 056 | (SPARE) | 167 |  |  |
| 104 | PF19 | 023 | 1 | 261 |  |  |
| 105 | PF 20 | 024 | 2 | 262 |  |  |
| 106 | PF21 | 025 | 3 | 263 |  |  |
| 107 | PF47 | 057 | (SPARE) | 166 |  |  |

Table 1-5. Model 5789 Key Codes (Cont)

| REY |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| NO. | SYMBOL | UNSHIFTED | SAIFTED |  | CODE | SYMBOL |

Certain keys (indicated by an asterisk in the KEY NO. Column of table 1-5 have a feature called Typanatic. This is an automatic repeat function. When you hold any one of these keys down, it automatically repeats at a rate of 10 times per second, after an initial half-second delay.

The Typamatic function is effective in both shifted and unshifted modes. It is not effective when you press the ALT key.

The keyboard also provides two audible tones. The alarm tone is activated by a code received from the terminal controller. The alarm tone is automatically shut off after 20 seconds. The alarm tone may also be shut off before 20 seconds elapses, by a shut-off code received from the terminal controller. The second audible tone corresponds to the TTY bell function, and is activated by a code received from the terminal controller. The TTY bell tone lasts about one-half second. The two audible tone frequencies are sufficiently different to allow immediate recognition of one from the other.

All power required to operate the model 5789 keyboard is supplied by the teminal controller.

Table 1-6 lists the keyboard specifications. The model 5789 keyboard is a commercial unit built to Sanders specifications. The keyboard is supplied under manufacturer's warranty to Sanders and is not considered field maintainable.

Table 1-6. Model 5789 Keyboard Specifications

| PARAMETER | CHARACTERISTIC |
| :--- | :--- |
| Alphanumeric/fixed function <br> keys | One alternate action and 71 momen- <br> tary action, of which 50 areSHIFT <br> key affected and 30 are ALT key <br> affected |
| Programmable function keys | 36 momentary action, of which 16 are <br> SHIFT key affected |

Table 1-6. Model 5789 Keyboard Specifications (Cont)

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| Power | $\begin{aligned} & +5 \mathrm{~V}, \pm 1 \%, \quad 850 \mathrm{~mA} \text { maximum } \\ & +15 \mathrm{~V}, \pm 1 \%, 50 \mathrm{~mA} \text { maximum } \\ & -15 \mathrm{~V}, \pm 1 \%, 50 \mathrm{~mA} \text { maximum } \end{aligned}$ |
| Output levels | Logic low is 0.0 V to +0.45 V Logic high is +2.45 V to +5.25 V |
| Output signal characteristics | Serial, RS-232 compatible, negative true |
|  | - Rest $=$ low |
|  | - Logic $1=10 w$ |
|  | - Code $=10$ bits (one start bit, eight data bits, one stop bit) |
| Maximum cable length (keyboard to terminal controller) | - (To be determined) |
| Operating temperature range | $+40^{\circ} \mathrm{F}$ to $+120^{\circ} \mathrm{F}$ |
| Storage temperature range | $-40^{\circ} \mathrm{F}$ to $140^{\circ} \mathrm{F}$ |
| Relative humidity | 10\% to 90\%, noncondensing |
| Storage pressure range | 19 inches to 32 inches, Hg |
| Dimensions | Length 20.5 inches ( 52.1 cm ) |
|  | Width 10.0 inches ( 25.4 cm ) Height 3.25 inches ( 8.3 cm ) |
| Weight | (To be determined) |

1.4.2 MODEL 5786 TRACKBALL AND MODEL 5787 FORCESTICK PHYSICAL DESCRIPTION. The trackball and forcestick are position entry devices (PEDS). A PED provides position input data to the terminal controller and controls the position of a cursor on the screen of a display monitor. The model 5786 trackball and the model 5787 forcestick are shown in figure 1-10.

Each PED is self contained in a black plastic case designed for desk top or similar use. Except for the means by which they are controlled, the two units are physically identical.

The trackball is controlled by means of a phenolic composition ball that can be rotated continuously in any direction. Interfacing with the internal electronics is accomplished by two digital shaft encoders that are turned by the ball as it is rotated.
NEG 79-456-007
MODEL 5787
FORCESTICK

## MODEL 5786 <br> TRACKBALL

Figure l-10. Model 5786 Trackball and Model 5787 Forcestick

The forcestick is controlled by means of spring-loaded rod approximately one inch long that can be deflected in any direction. Interfacing with the internal electronics is accomplished by strain gauges that are actuated when the rod is deflected.

Table 1-7 lists the specifications for both the trackball and the forcestick. Both the trackball and forcestick are commercial units built to Sanders' specifications. Each is supplied with a six-foot, permanently attached cable. A l5-pin connector on the cable plugs into one of the jacks on the system interconnect panel used with the GRAPHIC 8 terminal controller. The units are supplied to Sanders under manufacturer's warranty and are not field repairable or adjustable.

Table 1-7. PED Specifications

|  | TRACKBALL | FORCESTICK |
| :---: | :---: | :---: |
| Electrical Interface (to Teminal Controller) <br> Code <br> Signal Levels | 256-bit binary words, 960 RS-232C | baud, asynchronous |
| Word Repetition Rate | 37.5 Hz |  |
| Cursor Movement | 300 display elements per revolution of ball | 1280 display elements per second maximum |
| Resolution | Within one display element in 1024 |  |
| Force to Move Cursor | 15 grams (typical) | 37 grams, minimum cursor movement <br> 909 grams, maximum cursor movement |
| Power Requirements | $\begin{aligned} & +5 \mathrm{~V}, \\ & \pm 15 \mathrm{~V}, \\ & +20 \mathrm{~mA} \end{aligned}$ | $\begin{array}{ll} +5 \mathrm{~V}, & 450 \mathrm{~mA} \\ \pm 15 \mathrm{~V}, & 20 \mathrm{~mA} \end{array}$ |
| Operating Temperature | $15^{\circ} \mathrm{C}$ to $40^{\circ} \mathrm{C}\left(59^{\circ} \mathrm{F}\right.$ - to $\left.104^{\circ} \mathrm{F}\right)$ |  |
| Humidity | 10\% to 90\% |  |
| Connector | Cannon DA-15P |  |
| Cable | 6 feet supplied, 50 feet maximum length |  |
| Size | Height - 3.3 inches Width - 5.0 inches Depth - 7.5 inches |  |

Table 1-7. PED Specifications (Cont)

|  | TRACKBALI | FORCESTICK |
| :--- | :--- | :--- |
| Weight | 3.0 pounds <br> $(1.36 \mathrm{~kg})$ | 2.5 pounds <br> $(1.14 \mathrm{~kg})$ |
| Mechanical Action | Eriction drive, spring <br> loaded | Spring return <br> to center |

1.4.3 MODEL 5788 DATA TABLET PHYSICAL DESCRIPTION. The data tablet is a position entry device used to move a cursor and/or data displayed on a display monitor. The data tablet is shown in figure $1-11$.


NEG 81-108-001
H-82-0176-010

Figure 1-11. Data Tablet
The model 5788 data tablet has a formica surface that is housed in a steel and aluminum frame designed for desk top or similar use. The data tablet is a digitizer that uses a system of magnetic coupling between an electronic pen stylus transducer and an active surface to resolve positional coordinates. Movement initiated by the data tablet is proportional to the speed and direction in which the pen stylus is moved along the active surface. The data tablet may also be used to plot individual $X, Y$ coordinate pairs.

Switch selection of the output and operation modes is provided on the data tablet rear panel. Two connectors are also provided on the rear panel: one for the output circuits and one for the pen stylus. All. power required to operate the data tablet is supplied from the terminal controller and is applied to the data tablet via the rear panel output circuit connector.

Table 1-8 lists the specifications for the data tablet. The data tablet is a commercial unit built to Sanders' specifications. The data tablet is supplied under manufacturer's warranty to Sanders and is not field repairable or adjustable.

Table 1-8. Data Tablet Specifications

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| Electrical |  |
| Active surface area (in.) | 11 \% 11 |
| Total surface area (in.) | $12.8 \times 12.8$ |
| Resolution (lines/in.) |  |
| Accuracy (in.) | $\pm 0.025$ |
| Coordinate refresh rate (pr/sec) | 120 (standard) <br> 240 (optional) |
| Operating modes | Point, run |
| Transducer | Pen stylus |
| Output configurations | Option 1 |
|  | 8-bit parallel, binary and $B C D$ Option 2 |
|  | Serial, TTL, and RS232 Option 3 |
|  | Full parallel, binary |
| Nominal Power Requirements | 15 Vac at 0.15A |
| Operational temperature range | $15^{\circ}$ to $40^{\circ} \mathrm{C}$ |
| Mechanical |  |
| Tablet: |  |
| Weight (1b) | 7.25 |
| Enclosure | Steel and aluminum |
| Size (in.) | $\begin{aligned} & 15.66 \times 15.24 \times(0.76 \text { front; } 2.14 \\ & \text { rear) } \end{aligned}$ |

Table 1-8. Data Tablet Specifications (Cont)

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| Pen stylus assembly: |  |
| Length (in.) | 5.5 |
| Wiath (in.) | 0.5 |
| Cable (in.) | 40 |

1.4.4 MODEL 5781 PHOTOPEN PEYSICAL DESCRIPTION. The model 5781 pHoTOPEN is a lightweight, hand-held data entry device that uses a light detecting circuit to produce a TTL pulse output. The PHOTOPEN is shown in figure 1-12.


NEG 81-1284-001 H-82-0176-011

Figure 1-12. PHOTOPEN
The housing of the pHOTOPEN unit is made from two concentric metal cylinders, mechanically connected so that the outer shell is pushed back from the tapered front end of the inner cylinder. When the operator holds the unit by its outer shell and pushes the unit against the display monitor CRT screen hard enough, a switch circuit inside the unit produces a steady-state logic level output.

The tapered end of the inner cylinder houses a lens assembly that focuses light from the target area of the display monitor CRT screen onto a photosensitive circuit. The light causes the circuit to produce a low-going logic pulse that confirms the PHOTOPEN strike.

All electronic components of the PHOTOPEN are mounted on a 0.4 x 5.0 inch circuit card contained within the hand-held unit. An end cap at the rear of the hand-held unit provides structural rigidity and forms a gromet for the electrical cable that connects the PHOTOPEN unit to the teminal controller. Color-coded conductors within the cable carry the power required to operate the PHOTOPEN and provide for output signal interfacing. The conductors are soldered to terminal pins on the PHOTOPEN circuit card at one end; the other end of the cable has a 5 -pin connector that mates with one of two PPNn connectors on the terminal controller or system interconnect panel.

Table 1-9 lists the specifications for the PHOTOPEN unit.
Table 1-9. PHOTOPEN Specifications

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| Response time | 500 ns, maximum (measured with 40 ft-L with 150 ns pulse on P31 @ 60 Hz ) |
| Sensitivity | $10 \mathrm{ft}-\mathrm{L}$ with 150 ns pulse on P31 @ 60 Hz , typical |
| Immunity | No false triggering caused by normal fluorescent or incandescent lamps producing $100 \mathrm{ft-L}$ at display level |
| EMI | Immune to normal computer and display system EMI |
| Focal length | Optimized for 3/4-inch length from lens to display screen phosphor plane |
| Spectral response | Approximately 4,000A to 11,000A |
| Power input | ```PHOTOPEN Only: +5V @ 25 mA With intensifier/driver assembly: +5V @ 75 mA``` |
| Pulse output | TTL signal compatible, as follows: <br> Logic high level with no detected light-pulse input |
|  | Logic low pulse of approximately, 1.0-us duration when struck by CRT light beam, repeated at CRT refresh intervals if held within target area |

Table 1-9. PHOTOPEN Specifications (Cont)

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| Manual switch | Solid-state switch activated by pressing PHOTOPEN tip against CRT face to push inner assembly back into outer shell |
| Switch output | TTL signal compatible, as follows (fanout 2 , maximum load): |
|  | Logic high level with outer shell in relaxed state |
|  | Logic low level while outer shell is pushed forward |
| Size | Diameter: 0.6 inch; length: 6.0 inches |
| Weight <br> (incl. cable) | 4.0 ounces |
| Temperature (operating) | $+15^{\circ} \mathrm{C}$ to $+40^{\circ} \mathrm{C}$ |
| Relative humidity | 30\% to $90 \%$ |

### 1.5 OUTPUT DEVICES PHYSICAL DESCRIPTION

A variety of output devices is available for use with GRAPHIC 8 systems. These include color display monitors, monochrome display monitors, a monochrome hard copy unit, and a color hard copy unit. The following paragraphs describe each of these output devices in more detail.
1.5.1 COLOR MONITOR PHYSICAL DESCRIPTION. All the color display monitors available for use with GRAPHIC 8 systems use a 19 -inch (diagonally measured) picture tube and provide raster scan displays. Both standard and long persistence phosphor, and in-line and delta gun tube models are available. A typical color monitor is shown in figure 1-13.

All the color monitors available accept standard red, green, and blue ( $R G B$ ) video inputs; all inputs are typically terminated into 75 ohms. Each color monitor includes a sync stripper circuit to separate the sync signal from the green (G) signal; however, external sync may also be used. All the color monitors available will operate at various horizontal scan frequencies to support the various resolutions required.

The color monitors are usually supplied in cabinet enclosures for desk top or similar use. However, the color monitors may also be


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supplied without enclosures, with sliders and other mounting hardware as needed for installation in EIA standard 19-inch equipment racks.

Table 1-10 identifies the different models of color monitors. Table l-11 lists general perfomance specifications that apply to all models. The color monitors are comercial units built to Sanders' specifications and are supplied to Sanders under manufacturer's warranty.

Table 1-10. Color Monitor Model Numbers
MODEL NUMBER

| RESOLUTION <br> (SEE NOTE) | DELTA GUN, DESK TOP | DELTA GUN, RACK MOUNT | IN-LINE GUN, DESK TOP | IN-IINE GUN, RACK MOUNT |
| :---: | :---: | :---: | :---: | :---: |
| NOTE: All interlaced resolutions require long persistence phosphor. Noninterlaced resolutions use standard phosphor. |  |  |  |  |
| $\begin{aligned} & 512 \times 512 \\ & \text { interlaced } \end{aligned}$ | 8315 | 8415 | 8515 | 8615 |
| $512 \times 512$ noninterlace | 8316 | 8416 | 8516 | 8616 |
| $\begin{aligned} & 640 \times 480 \\ & \text { interlaced } \end{aligned}$ | 8325 | 8425 | 8525 | 8625 |
| $\begin{aligned} & 640 \text { x } 480 \\ & \text { noninterlaced } \end{aligned}$ | 8326 | 8426 | 8526 | 8626 |
| $\begin{aligned} & 1024 \times 768 \\ & \text { interlaced } \end{aligned}$ | 8335 | 8435 | 8535 | 8635 |
| $\begin{aligned} & 1024 \times 1024 \\ & \text { interlaced } \end{aligned}$ | 8345 | 8445 | 8545 | 8645 |

Table 1-11. Color Monitor Specifications

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| Input power voltage <br> Input power frequency <br> Power consumption | 100 Vac, 110 Vac, $120 \mathrm{Vac}, 220$ Vac, or 240 Vac, selectable, depending on model <br> Typically 105 Vac to 125 Vac 50 Hz or 60 Hz <br> Typically 47 Hz to 63 Hz 280 VA, maximum |

Table l-11. Color Monitor Specifications (Cont)

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| Input signals | R, G, B video |
|  | Typically l. OV peak-to-peak |
| Input connectors | BNC |
|  | Terminated into 75 ohms or high impedance (switch selectable) |
| Scanning frequency | Horizontal: 15 kHz to 35 kHz , depending on model |
|  | Vertical: Typically 60 Hz |
| Maximum screen size | $400 \mathrm{~mm} \times 300 \mathrm{~mm}$ |
| Video bandwidth | 20 MHz or 40 MHz , depending on model |
| Pulse response, rise and fall time | Typically better than 20 nanoseconds for 20 MHz bandwidth |
|  | Typically better than 11 nanoseconds for 40 MHz bandwidth |
| Black level stability | Typically $\pm 1 \%$ over $10 \%$ to $90 \%$ average picture level (ARL) |
| Convergence | Typically better than 1.0 mm over the entire screen |
| Raster size distortion | Typically less than $1 \%$ of raster height |
| Linearity | Typically better than 7\% |
| Warm-up time | Typically 20 minutes to 30 minutes for stable display |
| Ambient operating temperature | Typically $0^{\circ} \mathrm{C}$ to $40^{\circ} \mathrm{C}$ |
| Storage temperature | Typically $-10^{\circ} \mathrm{C}$ to $60^{\circ} \mathrm{C}$ |
| Relative humidity | Typically $10 \%$ to $80 \%$, non-condensing |
| Height | 366 mm to 443 mm , depending on model |

Table 1-11. Color Monitor Specifications (Cont)

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| Width | 470 mm to 485 mm, depending on <br> Depth <br> Weight |
| 480 mm to 545 mm, depending on <br> model |  |

1.5.2 MONOCHROME MONITOR PHYSICAL DESCRIPTION. The optional monom chrome monitors available for use with GRAPHIC 8 systems provide high resolution, raster scan displays of both interlaced and non-interlaced formats. All the monochrome monitors use a 19 -inch (diagonally measured) picture tube; both standard and long persistence phosphor models are available. A typical monochrome monitor is shown in figure 1-14.

All the monochrome monitors accept either EIA standard RS170 or EIA standard RS343 composite video inputs. Preset contrast and brightness controls are included on all configurations.

The monochrome monitors are available with or without a cabinet enclosure, and may be equipped with sliders for rack mounting, if desired. Table 1-12 identifies the different models of monochrome monitors. Table 1-13 lists the performance specifications for the monochrome monitors. The monochrome monitors are commercial units built to Sanders' specifications and are supplied to Sanders under manufacturer's warranty.

Table 1-12. Monochrome Monitor Model Numbers



Figure 1-14. Monochrome Monitor, 19-inch (Typical)

Table 1-13. Monochrome Monitor Specifications

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| Controls | Contrast with preset. Brightness with preset. Power on/Off |
| Inputs | BNC type input connectors are provided with switchable high $Z$ or 75 ohm termination. |
| Signal input | Composite video input $1.0 \mathrm{~V} p-\mathrm{p}$ nominal ( 0.35 to 2.0 acceptable) Sync negative. |
|  | Input standards are RSI70 compatible for 15.75 kHz operation or RS343 for 37 kHz operation. |
|  | Return loss: greater than 40 dB . <br> Common mode rejection: greater than 50 dB for $50 / 60 \mathrm{~Hz}$ power frequency. |
| Deflection characteristics | Scan rates: 15.750 kHz through 36.75 kHz interlaced or noninterlaced. Scanning rates are determined by adjustment on scan module. |
|  | Horizontal retrace: 5 usec nominal Vertical retrace: 600 usec nominal <br> Interlace performance: better than 90\%. |
|  | Raster size regulation: less than I\% change from 0\% to 100\% APL at 50 FL. |
|  | Scan failure protection: High voltage is switched off in the event of horizontal or vertical scan failure. |
| Video characteristics | Video bandwidth: -3 dB at 35 MHz <br> Pulse performance: $T R=10 \mathrm{nsec}$, $T E=13 \mathrm{nsec}$ |
|  | Line distortion: less than 1\% Field distortion: less than 1\% |

Table I-13. Monochrome Monitor Specifications (Cont)

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
|  | Black level stability: black level shift less than $1 \%$ change of peak luminance from $10 \%$ to $90 \%$ APL |
| Display performance | Picture tube igv type $90^{\circ}$ deflection Geometric distortion: $\pm 1 \%$ of raster height |
|  | Scanning linearity: $\pm 1 \%$ of raster height |
| Actual display size | $\begin{aligned} & 1176.1 \mathrm{sq} \cdot \mathrm{cm.}(180.0 \mathrm{sq} . \mathrm{in.}) \\ & 29.7 \mathrm{~cm} \mathrm{high}(11.6 \mathrm{in.}) \\ & 39.6 \mathrm{~cm} \text { wide }(15.5 \mathrm{in} .) \end{aligned}$ |
|  | Aspect ratio adjusted at factory 4 to 3 underscan. Other aspect ratios available on special order. |
| Brightness | Maximum usable brightness 150 FL Calibration 50 FL. |
|  | Calibration is achieved with beightness at visual raster cutoff and contrast adjusted with a flat field signal. |
| Typical display resolution 1225 line scan | 1280 horizontal by 960 vertical |
| Power requirements | $100 \mathrm{VAC} \pm 10 \%$ |
|  | 117 VAC $\ddagger 10 \%$ |
|  | $200 \mathrm{VAC} \ddagger 10 \%$ |
|  | 220 VAC $\ddagger 10 \%$ |
|  | $234 \mathrm{VAC} \ddagger 10 \%$ |
| Frequency | $50-60 \mathrm{~Hz} \pm 10 \%$ |
| Power consumption | 240 Watts nominal |
| Environmental |  |
| Temperature | $0^{\circ}$ to $50^{\circ}$ |
| Humidity | 5\% to 90\% non-condensing |
| Altitude | Normal operation to 10,000 feet |

Table 1-13. Monochrome Manitor Specifications (Cont)

| PARAMETER | CHARACTERISTIC |
| :--- | :--- |
| Safety | Per applicable DHHS, UL, CSA as date <br> of manufacture <br> Weight <br> Dimensions |
|  | 86 pounds |
|  | Rackmount 19 inches wide, 15.70 <br> high, 22.3 deep <br> Cabinet 19 inches wide, 15.70 high, <br> 22.3 deep |

1.5.3 HARD COPY UNIT PHYSICAL DESCRIPTION. Both monochrome and color hard copy units are available. The paragraphs that follow describe each of these units in more detail.
1.5.3.1 Monochrome Hard Copy Unit. A monochrome hard copy unit produces paper copies from standard, raster scan video signals of refreshed graphic terminals. The unit produces high contrast black and white or gray scale copies within seconds, using a clean, dry process. The unit is completely self contained, has a built-in stacking tray, and is mounted on casters for portability. A typical monochrome hard copy unit is shown in figure 1-15.


Figure 1-15. Monochrome Hard Copy Unit

The monochrome hard copy is easily interfaced to a graphic system or refreshed terminal by one coaxial cable and one control cable. Operation is as simple as pushing a button. Remote operation is provided for, allowing control from a simple remote switch or from a complex computer system, under program control.

The monochrome hard copy unit developing uses $3 M$ brand, type 777 dry-silver paper, The process is dry and stable and allows clean, conventional paper handling.

Maintainability is built in. All electrical and mechanical assemblies are easily accessible. Circuit boards and modules can be easily removed or placed on an extender board for maintenance. The transport mechanism is virtually jam-free.

Table 1-14 lists the principal differences between the different models of monochrome hard copy units. Table l-15 lists the performance specifications for the monochrome hard copy unit. The monochrome hard copy unit is, a commercial equipment built to Sanders' specifications and is supplied to Sanders under manufacturer's warranty.

Table 1-14. Principal Differences Between Monochrome Hard Copy Units

| MODEL | RESOLUTION SUPPORTED |
| :---: | :---: |
| 8570 | $512 \times 512$, interlaced |
| 8571 | $512 \times 512$, non-interlaced |
| 8572 | $640 \times 480$, interlaced |
| 8573 | $640 \times 480$, non-interlaced |
| 8574 | $1024 \times 768$, interlaced |
| 8575 | $1024 \times 1024$, interlaced |

Table 1-15. Monochrome Hard Copy Unit Specifications

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| Paper size (nominal) | $8-1 / 2 \times 11$ inches ( $21.6 \times 27.9 \mathrm{~cm}$ ) |
| Image size |  |
| Rectangular format Square format | $6-3 / 8 \times 8-1 / 2$ inches ( $16.2 \times 21.6 \mathrm{~cm}$ ) $7-1 / 2 \times 7-1 / 2$ inches $(19.1 \times 19.1 \mathrm{~cm})$ |
| Image characteristics Gray scales Resolution | 5 distinguishable levels (minimum) 100 lines per inch |
| Copy time (approximate) First copy | 18 seconds ( 7 second exposure, 11 second process) |
| Additional copies | 8 second intervals |
| Warmup time | 10 minutes |

Table 1-15. Monochrome Hard Copy Unit Specifications (Cont)

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| Input signal requirements |  |
| Video | 0.3 to 5 V p-p at 75 ohms |
| Sync | 0.3 to 8 V p-p at 20 K ohms |
| Remote copy | A logic low (ground closure $>200$ us) initiates a copy command |
| Output signal characteristics End of copy | Open collector; 10 to 20 us low after end of copy exposure time |
| Power | ```105 to 125 Vac 50/60 Hz 1000 watts maximum, }500\mathrm{ watts standby``` |
| Dimensions |  |
| Height | 42 inches ( 106.7 cm ) |
| Wiath | 23 inches ( 58.4 cm ) |
| Depth | 30 inches ( 76.2 cm ) |
| Weight | 275 pounds ( 124.7 kg ) |
| Environmental |  |
| Temperature, operating | $+15^{\circ} \mathrm{C}$ to $+35^{\circ} \mathrm{C}$ |
| Temperature, non-operating | $-15^{\circ} \mathrm{C}$ to $+55^{\circ} \mathrm{C}$ To 90 \%, non-condensing |
| Paper life (typical) | Approximately 6 months at $20^{\circ} \mathrm{C}$ Approximately 15 days at $30^{\circ} \mathrm{C}$ |
| Multiple input switching | Optional |
| Motor and temperature control |  |
| Temperature adjustment range | $120^{\circ} \mathrm{C}$ to $150^{\circ} \mathrm{C}$ |
| Stability | $\pm 2^{\circ} \mathrm{C}$ of set point at sensor location |
| Thermal cut-out | $168^{\circ} \mathrm{C}$ |
| Motor control | Speed adjustable to 2 inches/second; maintains speed at $\pm 3 \%$ of set speed under all conditions |

Table 1-15. Monochrome Hard Copy Unit Specifications (Cont)

| PARAMETER | CHARACTERISTIC |
| :--- | :--- |
| Idle speed | Approx $1 / 2$ set speed |
| Fuses |  |
|  |  |
| Line | 8A fast |
| Electronics | 1 A slow |
| High voltage | 0.6 A slow |
| +400V (2 ea) | $1 / 16 \mathrm{~A}$ fast |
| Deflection ampl | 1.5 A fast |

1.5.3.2 Color Hard Copy Unit. A color hard copy unit is a standalone recording device for making $8 \times 10$ inch color or black and white prints and 35 mm slides of any data presented on a high resolution, raster scan CRT. A typical color hard copy unit is shown in figure 1-16.

The color hard copy unit features exceptional image resolutions with accurate rendition of color hue, saturation, and lightness; high throughput; separation mode for automatic three-color separation exposures; and switchable raster blending for high color saturation prints. The color hard copy unit is microprocessor controlled to assure consistent exposure and reliable operation. Fully automated 35 mm slide capabilities are also provided for, with remote control.

Two models are currently available: model 8580 and model 8581. Model 8580 supports non-interlaced resolution formats and model 8581 supports interlaced resolution formats. Table l-16 lists the performance specifications for the color hard copy unit.

The color hard copy unit is a commercial equipment built to Sanders specifications and is supplied to Sanders under manufacturer's warranty.

Table 1-16. Color Hard Copy Unit Specifications

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| Video monitor <br> Speed of operation | Nominal resolution of 1400 lines center screen at $100 \mathrm{~cd} / \mathrm{m}^{2}(30 \mathrm{FL})$ on flatface CRT. <br> Pixel position error is <0.5\% within a 9 cm circle, <l\% at corners. <br> 8" by $10 "$ Polaroid - less than 60 seconds per exposure <br> $8^{\prime \prime}$ by $10^{\prime \prime}$ transparency - less than 40 . seconds per exposure <br> $35 \mathrm{~mm}-6$ seconds per exposure |



Figure 1-16. Color, Hard Copy Unit

Table 1-16. Color Hard Copy Unit Specifications (Cont)

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| Film type | $8^{\prime \prime} \times 10^{n}(20.32 \mathrm{~cm} \times 25.40 \mathrm{~cm})$ - color <br> or black and white. Must be loadable in cassette format. <br> With optional auxiliary camera, 35 mm sprocket feed film, color only, in. cartridge. |
| Film speed | 15 DIN (ASA25) to 24 DIN (ASA200) |
| Physical specifications | Width 38.7 cm ( 15.25 in.$)$ |
|  | Depth 38.7 cm ( 15.25 in.$)$ |
|  | Height 113.2 cm ( 44.75 in.$)$ |
|  |  |
|  | Weight 40 kg ( 88 lbs. ) |
| Power requirements (all units single phase, line to neutral) | Standard $120 \mathrm{VAC} \pm 10 \%$. $50 / 60 \mathrm{~Hz}$, 0.8 amps. 110 watts (max) |
|  | Optional $100 \mathrm{VAC} \pm 10 \%, 50 / 60 \mathrm{~Hz}$. 1.0 amps: 220 or 240 VAC $\pm 10 \%, 50 / 60 \mathrm{~Hz}, 0.5 \mathrm{amps}$ |
| Video inputs | Separate red, green, blue, and sync video signals of 0.35 to 2.0 peak to peak voltage required. |
|  | Standard - Handles horizontal line rate of 500 to 650 . |
|  | Optional-High Line - Handles horizontal line rate of 800 to 1100 at 60 Hz or 1300 at 50 Hz . |
| Operating environment | Temperature - $20^{\circ} \mathrm{C} \pm 10^{\circ} \mathrm{C}$ |
|  | Relative Humidity - $15 \%$ to $90 \%$ non-condensing |
|  | Altitude - Sea level to 4500 meters Operating Noise - Negligible |
| Cables | Power: 3 meter power cord. |

Table 1-16. Color Hard Copy Unit Specifications (Cont)

| PARAMETER | CHARACTERISTIC |
| :---: | :--- |
|  | Interface: Four 74 Ohm, RG-m9 coaxial <br> cables of 3 meters each. BNC plug on <br> each end mates to BNC bulkhead <br> receptacle. |

1.6 POWER AND ENVIRONMENTAL REQUIREMENTS

The terminal controller requires 300 W of single-phase primary power. The power source must be within six cable-feet of the terminal controller.

The terminal controller fits a 10.5 -inch vertical space in a standard 19-inch equipment rack, either directly or on slides. The controller can also be supplied as a stand-alone cabinet unit.

The operating environment temperature range is $+15^{\circ} \mathrm{C}\left(59^{\circ} \mathrm{F}\right)$ through $+40^{\circ} \mathrm{C}\left(104^{\circ} \mathrm{F}\right)$. The relative humidity should not exceed $90 \%$.

### 1.7 PERFORMANCE SPECIFICATIONS

Table 1-17 lists the performance specifications for the overall terminal controller and its assemblies where applicable.

Table 1-17. GRAPHIC 8 Terminal Controller Specifications

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| GENERAL |  |
| Power source | $\begin{aligned} & 115 \pm \frac{10 \mathrm{Vac}}{47} \text { to } 63 \mathrm{~Hz} \end{aligned}$ |
| Power | 300 Watts |
| Temperature: Storage | $0^{\circ}$ to $50^{\circ} \mathrm{C}$ |
| Operating | $15^{\circ}$ to $40^{\circ} \mathrm{C}$ |
| Relative humidity | 10 to $90 \%$ |
| Dimensions: |  |
| Rack mount configuration |  |
| Height | $10.5 \mathrm{in}(26.8 \mathrm{~cm})$ |
| Width | 19.0 in ( 48.2 cm ) |
| Depth | 16.0 in ( 40.6 cm ) |

Table 1-17. GRAPHIC 8 Terminal Controller Specifications (Cont)

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| GENERAL (Cont) |  |
| Weight <br> Equipment cabinet configuration <br> Height <br> Width <br> Depth <br> Weight | ```55 1bs (25 kg) including cards 30 in (76.2 cm) 23 in (58.4 cm) 30 in (76.2 cm) 155 lbs (70.3 kg) on four casters``` |
| DISPLAY PROCESSOR |  |
| General purpose microprocessor <br> Word length <br> Byte mode <br> Instructions <br> Registers <br> Hardware stacks <br> Automatic priority interrupt <br> Memory <br> ROM <br> RAM | Yes <br> 16 bits <br> 8 bits <br> 400 plus <br> 8 <br> Yes <br> Yes <br> 16 bit words <br> 8192 words <br> to 262,144 words |
| INTERFACE OPTIONS (DIGITAL) |  |
| Parallel <br> Serial <br> Parallel microprocessor <br> Display instructions <br> Synchronized linkage to display processor | ```16 bits 32 bits (optional) RS-232C, RS449 1 6 ~ b i t s 50 plus Yes``` |

Table 1-17. GRAPHIC 8 Terminal Controller Specifications (Cont)

| PARAMETER | CHARACTERISTIC |
| :---: | :---: |
| INTERFACE OPTIONS (DIGITAL) (Cont) |  |
| Subroutine stack <br> Display registers <br> Registers (GP) | Yes <br> 64 plus <br> 4 |
| VECTORS/CONICS |  |
| Line texture Ellipse | 4 any angle |
| CHARACTERS |  |
| Font size <br> Character set (standard) <br> User defined (optional) <br> Rotation <br> Sizes <br> Tabular characters <br> Positioning | ```5\times7 7\times9 96 96 90. clockwise 3 Auto text spacing Random``` |
| MAPPING MEMORY |  |
| Addressable locations Bits/pixel | $\begin{aligned} & 2048 \times 2048 \\ & 1,2,4,8 \end{aligned}$ |
| VIDEO CO |  |
| Blink <br> Color or gray level <br> Screen splits <br> Cursor <br> Teminations | Yes <br> 256 <br> 3 <br> non-destructive <br> 75 ohm |

Table 1-17. GRAPHIC 8 Terminal Controller Specifications (Cont)

| PARAMETER | CHARACTERISTIC |
| :--- | :---: |
| VIDEO CONTROLLER (COnt) |  |
| Video | Composite |
| Displays max | 6 |

1.8 EQUIPMENT IDENTIFICATION

The part number of the terminal controller is a function of its card complement and thus varies from installation to installation. The Sanders identification plate at the rear of the terminal controller carries the part number, voltage rating, current rating, and UL, CSA, and VDE identification.

Nomenclatures and part numbers for the circuit cards are etched on the component side of the cards. Serial numbers are stenciled next to part numbers.

All correspondence and documentation concerning the terminal controller or its assemblies should include full identification data.

### 1.9 TEST EQUIPMENT REQUIRED

The following equipment (or equivalent) is recommended for maintenance of the terminal controller:

Oscilloscope Tektronix type 465 with 10x probe
Digital voltmeter . Fluke model 8000A
Card extender Sanders part no. 4171110

### 1.10 RELATED PUBLICATIONS

Publications relating to the GRAPHIC 8 system are as follows:
PUBLICATION NO.*

| H-80-0483 | GRAPHIC 8 Technical Description |
| :--- | :--- |
| H-80-0444 | GRAPHIC 8 Programmer's Reference Manual |
| H-81-0021 | GRAPHIC 8 Fortran Support Package Reference Manual |
| H-81-0027 | GRAPHIC 8 Terminal Controller Maintenance Manual |
| H-81-0097 | GRAPHIC 8 Terminal Controller Maintenance Diagrams | Manual

*This column lists the manual's basic number. Revisions are indicated on the cover of the manual by a letter following this basic number.

PUBLICATION NO.*
H-81-0162 GRAPHIC 8 19-Inch Color Display Manual
H-81-0129 GRAPHIC 8 Installation Planning Guide
H-80-0087 Teminal Controller Power Supply Model MM23-E0647/115

### 1.11 MNEMONICS CONVENTIONS

The convention established for naming mnemonics throughout this manual is as follows. In general, terminal controller memonics consist of six characters. The first four are an alphanumeric abbreviation of the signal name. The fifth is a sign ( + or - ) that indicates the active state of the signal (high or low). The sixth character is an alphabetic code that identifies the source of the signal as follows:

SOURCE CODE
B
$B$
$D$
$E$
$H$
$I$
$M$
$N$
$V$
$X$
$X$
$S$
$U$

## SOURCE

Processor bus signal Display processor ROM and status Digital graphic controller parallel interface Read/write memory Mapping memory Video controller Multiple source Multiport serial interface Timing module

Register and bus mnemonics use the lowest numeric to designate the least significant bit and the highest numeric to designate the most significant bit. For example, in the terminal controller data bus:

$$
\begin{aligned}
& \text { DAOO-B }=\mathrm{ISB} \\
& \text { DA15-B }=\mathrm{MSB}
\end{aligned}
$$

Power supply mnemonics are as follows:

| MNEMONIC | DESCRIPTION |
| :--- | :--- |
| P05V+ | +5 Vac |
| P15V | +15 Vdc |
| N15V- | -15 Vac |
| ARET- | Analog ground |
| DRET- | Digital ground |
| CGND | Chassis ground |

WThis column lists the manual's basic number. Revisions are indicated on the cover of the manual by a letter following this basic number.

### 2.1 GENERAL

This section contains information for operating the GRAPHIC 8 system. Topics discussed include: controls and indicators, turn-on procedure, and operation in the SYSTEM and LOCAL modes, and peripherals usage.

## 2. 2 CONTROLS AND INDICATORS

The following paragraphs describe the location and function of the controls and indicators on the various components of a typical GRAPHIC 8 system.
2.2.1 TERMINAL CONTROLIER CONTROLS AND INDICATORS. Table $2-1$ ists the terminal controller controls and indicators, their locations and functions. Figure $2-1$ shows their location on the equipment. Circuit connections for the controls and indicators are shown on the applicable terminal controller diagrams in the Terminal Controller Diagrams Manual (H-81-0097).

Table 2-1. Terminal Controller Controls and Indicators

| NOMENCLATURE | LOCATION | FUNCTION |
| :--- | :--- | :--- |
| POWER ON/OFF circuit <br> breaker | Control panel | Energizes/deenergizes <br> terminal controller |
| RUN/SYS pushbutton | Control panel | Initiates SYSTEM mode <br> (host computer <br> control) |
| DIS/LOC pushbutton | Control panel | In RUN/SYS pushbutton |
| Indicates display |  |  |
| processor card |  |  |
| operating |  |  |



$4.62 \cdot 00400: 7$
2.2.2 COLOR MONITOR CONTROLS AND INDICATORS. Table $2-2$ lists the controls and indicators for a typical color monitor, by their locations and functions. Figure $2-2$ shows their locations on the equipment. Circuit connections for the controls and indicators are shown in the applicable technical manual for the color monitor used in your GRAPHIC 8 system.

Table 2-2. Color Monitor Controls and Indicators

| NOMENCLATURE | LOCATION | FUNCTION |
| :---: | :---: | :---: |
| POWER ON/OFF switch | Bottom right of control panel | Energizes/deenergizes the color monitor |
| DEGAUSS switch | Bottom right of control panel | Demagnetizes picture tube |
| BRIGHTNESS control | Bottom right of control panel | Adjusts brightness of display presentation |
| MONO/COLOR switch | Bottom right of control panel | Selects color or monochrome operation |
| HEALTH Lamp | Top right of control paneI | Lights to indicate proper operation of the power supply circuits |
| TERMINATION switches | Center of rear panel | Selects 75 ohms or high impedance for video inputs |

2.2.3 MONOCHROME MONITOR CONTROLS AND INDICATORS. Table 2-3 lists the controls and indicators for a typical monochrome monitor, by their locations and functions. Figure $2-3$ shows their locations on the equipment. Circuit connections for the controls and indicators are shown in the applicable technical manual when a monochrome monitor is used in your GRAPHIC 8 system.
2.2.4 MONOCHROME HARD COPY UNIT CONTROLS AND INDICATORS. Table 2-4 lists the controls and indicators for a typical monochrome hard copy unit, by their locations and functions. Figure $2-4$ shows their locations on the equipment. Circuit connections for the controls and indicators are shown in the hard copy unit technical manual when a monochrome hard copy unit is used in your GRAPHIC 8 system.


Table 2-3. Monochrome Monitor Controls and Indicators

| NOMENCLATURE | LOCATION | FUNCTION |
| :---: | :---: | :---: |
| POWER switch | Bottom left of control panel | Energizes/deenergizes the monochrome monitor |
| BRIGHT control with PRESET OFF/ON switch | Bottom center of front panel | Adjusts brightness of display presentation; allows brightness level to be preset |
| CONT control with PRESET OFF/ON switch | Bottom right of front panel | Adjusts contrast of display presentation; allows contrast level to be preset |
| Termination switches | Center of rear panel | Selects 75 ohms or high impedance termination for video inputs |

Table 2-4. Monochrome Hard Copy Unit Controls and Indicators

| NOMENCLATURE | LOCATION | FUNCTION |
| :---: | :---: | :---: |
| POWER OFF/ON switch | Top of control panel (on top of unit) | Energizes/deenergizes the monochrome hard copy unit |
| LIGHT/DARR control | Center of control panel (on top of unit) | Varies the darkness of the delivered copy |
| COPY pushbutton | Bottom of control panel | Initiates production of a paper copy |
| SYNC EXT/INT switch | Top center of rear panel | Selects external or internal sync for operating the monochrome hard copy unit |
| VIDEO BACKGROUND <br> INVERT/NORM switch | Bottom center of rear panel | Selects inverted (black) or normal (white) background for copies produced |



Figure 2-3. Monochrome Monitor Controls and Indicators


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Figure 2-4. Monochrome Hard Copy Unit Controls and Indicators
2.2.5 COLOR HARD COPY UNIT CONTROLS AND INDICATORS. Table $2-5$ lists the color hard copy unit controls and indicators, by their locations and functions. Figure $2-5$ shows their locations on the equipment. Circuit connections for the controls and indicators are shown in the applicable technical manual when a color hard copy unit is used with your GRAPHIC 8 system.

Table 2-5. Color Hard Copy Unit Controls and Indicators

| NOMENCLATURE | LOCATION | FUNCTION |
| :---: | :---: | :---: |
| POWER On/Off switch | Bottom left of control panel | Energizes/deenergizes the color hard copy unit |
| POWER on indicator (green) | Integral with POWER on/off | Lights to indicate power on |
| RESET pushbutton (red) | Bottom left of control panel | Terminates all camera operations, causes filter wheels to seek and return to home, and enters data from internal program switches |
| RESET indicator | integral with RESET pushbutton | Lights during reset cycle |
| COLOR/FOCUS switch | Bottom left of center on control panel | In COLOR position, exposures are made on film when EXPOSE pushbutton is pressed. In FOCUS position, allows auxiliary cameras to be focused. |
| FOCUS indicator | Integral with NORMAL/ FOCUS switch | Lights to indicate FOCUS mode |
| SEPARATE/NORMAL switch | Bottom left of center on control panel | In NORMAL position, complete RGB exposure is made when EXPOSE pushbutton is pressed. In SEPARATE position, one color is exposed at a time. EXPOSE pushbutton must be pressed once for each color. |

Table 2-5. Color Hard Copy Unit Controls and Indicators (Cont)

| NOMENCLATURE | LOCATION | FUNCTION |
| :---: | :---: | :---: |
| SEPARATE indicator (yellow) | Integral with <br> SEPARATE/NORMAL switch | Lights to indicate SEPARATE mode |
| EXPOSE pushbutton | Bottom right of control panel | Starts the exposure cycle |
| EXPOSE indicator (green) | Integral with EXPOSE pushbutton | Lights to indicate that camera is able to take a picture |
| SEQUENCE indicator (red, green, blue) | Right of control panel | Displays the color of the video signal being photographed |
| AUX position indicator (yellow) | Bottom center of control panel | ```Lights when auxiliary port mirror is in position for auxiliary exposure mode.``` |
| $8 \times 10$ position indicator (yellow) | Middle center of control panel | ```Lights when auxiliary port mirror is in position for 8*10 exposure mode``` |
| OUT OF FILM indicator (red) | Top center of control panel | Lights when an expose command is sent to the 35 mm camera and no corresponding $x$ sync pulse is received from the camera |
| Film counter (EXPOSURE) | Bottom right of center on control panel | Five-digit electromechanical counter logs each auxiliary port exposure. Includes manual reset pushbutton. |



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Figure 2-5. Color Hard Copy Urit Controls and Indicators

### 2.3 TURN-ON PROCEDURES

Only the terminal controller, the display monitor, and the hard copy unit require turn-on. The remaining GRAPHIC 8 system components are all initialized when the terminal controller is turned on. The following paragraphs describe the steps required to set up and turn on the terminal controller, the display monitor, and the hard copy unit. These procedures assume that the GRAPHIC 8 system has been installed and that the system components have been properly interconnected, as described in Section 3 of this manual.
2.3.1 TERMINAL CONTROLLER. To turn the terminal controller on, press the 1 side of the POWER ON/OFF circuit breaker. This action lights the RUN/SYS lamp and applies power to the circuit cards.

Delay timers in the circuit cards allow initial power surges to settle, then initialize the terminal controller in the SYSTEM mode. All peripheral devices are reset, and the control program performs automatic diagnostic tests to verify operation of the basic terminal controller functions.

If the terminal controller is connected to a host computer that is already operating, the controller automatically transmits a performance status report to the computer. If the host computer is not operating, the computer can receive this status report only by initializing the terminal controller as part of its own turn-on procedure. In either case, the host computer's response to the status report is a function of the host computer application software.

## NOTE

Refer to Section 4 for maintenance information if (a) RUN/SYS lamp does not light following power turn-on; (b) host computer subsequently reports terminal controller defective; or (c) terminal controller operation is suspect for any reason.
2.3.2 COLOR MONITOR. To turn on a typical color monitor, proceed as follows:

1. Ensure that the MONO/COLOR switch is set to COLOR.
2. Set the POWER ON/OFF switch to ON. With the terminal controller in LOCAL mode, the verification test pattern (figure 2-6) should appear after approximately 30 seconds delay.
3. Ensure that the HEALTH indicator lamp lights, indicating a proper and safe high voltage level.
4. Adjust the BRIGHTNESS control for the desired viewing level.

NOTE
If the color purity or the convergence appears to be slightly out of adjustment, press and hold the DEGAUSS switch for several seconds, then release. The display image should improve noticeably. If the monitor does not display an image, or if the displayed image is out of adjustment and cannot be corrected by pressing the DEGAUSS switch, refer to the maintenance and alignment procedures given in the applicable technical manual.
2.3.3 MONOCHROME MONITOR. TO turn on a typical monochrome monitor, proceed as follows:

1. Press the POWER switch. With the terminal controller in LOCAL mode, the verification test pattern (figure 2-6) should appear after a brief delay.
2. Adjust the BRIGRT and CONT controls for the desired viewing level. Specific brightness and contrast levels may be preset by using the associated PRESET OFF/ON switches, if desired.

## NOTE

If the monitor does not display an image, or if the image is out of adjustment, refer to the maintenance and alignment procedures given in the applicable technical manual.
2.3.4 MONOCHROME HARD COPY UNIT. To set up and turn on a typical monochrome hard copy unit, proceed as follows:

1. Ensure that the POWER OFF/ON switch (on top of the unit) is set to OFF.
2. Lift the cover of the unit from the front and push it back until it latches in an upright position.
3. Pull the paper carriage down and out so that the hard copy unit CRT screen is visible.
4. Pull up on the cover interlock button (at the upper right rear corner) to bypass the safety cut-off.
5. Set the POWER OFF/ON switch to ON.

6. Adjust the LIGHT/DARK control (on top of the unit) to mid-scale.
7. Press the COPY pushbutton (on top of the unit). With the terminal controller in LOCAL mode, the verification test pattern should move across the face of the CRT in the hard copy unit.
8. Ensure that the paper drive mechanism and cutter bar are operating properly.
9. Set the POWER OFE/ON switch to OFF.
10. Load the paper cassette, following the instructions printed on the upper inside surface of the hard copy unit cover.
11. Set the POWER OFF/ON switch to ON.
12. Press the cover interlock button and hold for two seconds, to reset the safety cut-off.
13. Lift the cover latch and close the cover.
14. Allow the unit to warm up for 10 minutes.
15. Press the COPY pushbutton.

NOTE
The first copy will be black, due to exposure to light while loading.
16. Make several more copies and observe the results. Adjust the LIGHT/DARK control as required for best copy density.

NOTE
If the hard copy unit does not produce a reliable copy of the display image, or the LIGHT/DARK control has no effect, refer to the maintenance and alignment procedures given in the hard copy unit technical manual.
2.3.5 COLOR HARD COPY UNIT. To turn on and set up a typical color hard copy unit, proceed as follows:

1. Press the front control panel POWER pushbutton and allow the color hard copy unit to warm up for 20 minutes.
2. Open the color hard copy unit operator door (on the right-hand side of the unit). Make sure that you override the door interlock switch by pulling it all the way out.
3. Insert a properly loaded $8 \times 10$ film cassette into the cassette door at the top of the unit. Make sure the dark slide in the cassette is not pulled out. (Refer to the instruction sheet supplied with the box of film for details.)
4. Set the mode select lever to the $8 \times 10$ position.
5. Set the front control panel COLOR/FOCUS switch to COLOR.
6. Select an image that represents the maximum output of the graphics terminal red video signal. Since the color white is composed of the maximum values of red, green, and blue video, this can be accomplished by creating a white image on the graphics terminal. The image should be large enough for the "eye" of the color hard copy unit photometer probe to cover it.
7. Set all the calibration control panel BRIGBTNESS and CONTRAST potentiometers to 0 .
8. Set the calibration control panel photometer (FOOTLAMBERTS) switch to 2.
9. Press the front control panel RESET pushbutton.
10. Set the calibration control panel VIDEO RREVIEW switch to RED.
11. Place the photometer probe over the selected image area on the video monitor inside the color hard copy unit.
12. Adjust the calibration control panel RED BRIGHTNESS potentiometer until the dc meter on the panel reads 0.1. The phosphor should just be visible on the video monitor inside the unit.
13. Set the calibration control panel FOOTLAMBERTS switch to 50 and adjust the RED CONTRAST potentiometer until the dc meter on the panel reads 0.6 .
14. Write the setting of the RED CONTRAST potentiometer on a piece of paper. This corresponds to the red video contrast setting.
15. Disconnect the red video input cable from the connector marked RED on the color hard copy unit back panel.
16. Connect the green video input cable to the RED connector.
17. Press the front control panel EXPOSE pushbutton.
18. Adjust the calibration control panel RED CONTRAST potentiometer until the dc meter on the panel reads 0.6 .
19. Write the setting of the RED CONTRAST potentiometer on a piece of paper. This corresponds to the green video contrast setting.
20. Disconnect the green video input cable from the RED connector and reconnect it to the connector marked GREEN on the color hard copy unit back panel.
21. Connect the blue video input cable to the RED connector.
22. Repeat steps 18 and 19 for the blue video signal.
23. Disconnect the blue video input cable from the RED connector. Reconnect both the red video input cable and the blue video input cable to their respective connectors on the color hard copy unit back panel.
24. Set the calibration control panel RED, GREEN, and BLUE CONTRAST potentiometers to the settings recorded in steps 14,19 , and 22. Lock the potentiometers.
25. Press the front control RESET pushbutton.
26. Set the calibration control panel VIDEO PREVIEW switch to NORM.
27. Return the photometer probe to its stowage position and secure it in place.
28. Close and secure the operator door.
29. Press the front control panel RESET pushbutton.
30. Set the auxiliary ( 35 mm ) camera shutter control dial to B .
31. Set the auxiliary camera motor drive switch to $S$.
32. Set the auxiliary camera lens to $f / 4$.
33. Pull out the auxiliary camera film rewind knob and open the back cover.
34. Place a roll of film in the camera and push the film rewind knob back to its original position. Use standard 35 mm film (12, 20 , or 36 exposures).
35. Clip the end of the film tip onto the take-up spool.
36. Press the auxiliary camera magnetic ralease button and advance the film until the sprocket teeth catch properly in the perforations along both edges of the film. Close the back cover and press until it locks in place.
37. Press the magnetic release button and advance the film again. The white line on the film rewind knob should rotate when the film is advanced.
38. Press the front control panel EXPOSE pushbutton several times to skip past the leader on the film.
39. Set the front control panel exposure counter to zero to ensure an accurate count of the 35 mm exposures made.
40. Set the model select lever to AUX.
41. Set the front control panel COLOR/FOCUS switch to FOCUS.
42. Press the front control panel ExPOSE pushbutton. This unblanks the monitor for approximately 60 seconds.
43. Focus the auxiliary camera, using the through-the-lens viewer.
44. Return the front control panel COLOR/FOCUS switch to COLOR.

The color hard copy unit is now turned on, calibrated, and set up. Use the mode select lever to control whether 8 x lo instant color prints or 35 mm slides will be produced.

### 2.4 OPERATING PROCEDURES

The GRAPHIC 8 system normally operates in either the SYSTEM mode or the LOCAL mode. TYpically, in the SYSTEM mode the GRAPHIC 8 is under host computer control; in the LOCAL mode the GRAPHIC 8 functions as a stand-alone device. Operator performance. checks and various other diagnostics may be performed in the LOCAL mode.

The following paragraphs describe operation in the SYSTEM mode and the LOCAL mode in more detail.
2.4.1 SYSTEM MODE OPERATION. This mode is established when one of the following occurs:

1. When primary ac power is applied to the terminal controller.
2. When you press the RUN/SYS pushbutton.
3. When the terminal controller is in LOCAL mode and you type $s$ on the keyboard.
4. When the teminal controller is in LOCAL mode and you type 157760G RETURN on the keyboard.
5. When an initialize signal comes from the host computer via the parallel interface or the multiport serial interface (series 8000 only).
6. When the terminal controller is in the teletypewriter emulation mode (see paragraph 2.4.2.2.e) and you press Eunction key F13 on the keyboard or the host computer sends octal code 035 (ASCII control character GS Group Separator).
If the terminal controller is already in SYSTEM mode, it can be initialized again by either of the following:
7. A discrete initialize signal from the host computer via the parallel interface or the multiport serial interface (series 8000 only).
8. An IZ (initialize) message from the host computer.

Initialization in the SYSTEM mode automatically causes the built-in diagnostic routines to be performed and the results sent in an error status message to the host computer. The diagnostic routines include GO/NO-GO checks of the graphic controller; display processor; read/write memory; 2D or $3 D$ coordinate converter (if installed); and either the parallel interface or the multiport serial interface, whichever is used for communications with the host computer. The error status message also includes a checksum of the control program stored in read-only memory.

In the SYSTEM mode, responses to all operator actions are detemined by the application program of the host computer. Control is exercised and data is transferred by means of messages sent between the host computer and the terminal controller. See the GRAPHIC 8 Programmer's Reference Manual, Sanders document H-80-0444.

The host computer application program accesses all display registers and parameters for organization of display images. The initialization sequence enables the associated keyboards so you can enter commands without special action by the host computer.

The control program handles all internal display interrupts and operator inputs. The control program performs all housekeeping required for these events, and sends the host computer a message containing all information needed for operational decisions. However, the host computer can preset the terminal controller to transmit only specified signals under specified conditions.

The control program processes trackball, forcestick, data tablet, or pHoTOPEN inputs without host computer intervention. The control progran detects all PED (position entry device) inputs and either transmits them to the host computer, or uses them to update the position of a predefined $P E D$ identifier symbol on the display. Control program processing of PED symbols is controlled by the host computer application program.

The control program also inserts alphanumeric data from the keyboard into the refresh pattern; you can enter and edit a message without host computer intervention. You complete your entry by pressing the RETURN key, and the control program informs the computer that a new message is ready. The application program indicates how alphanumeric inputs are handled by issuing special commands.
2.4.2 LOCAL MODE OPERATION. After primary power has been applied to the terminal controller, you can initialize the terminal controller in LOCAL mode by pressing the DIS/LOC pushbutton. When you press this pushbutton, the verification test pattern appears on each of the associated display indicators, the terminal controller performs its built-in diagnostic routines, and LOCAL mode commands can be executed.

## NOTE

> When you press the DIS/LOC pushbutton, the built-in diagnostic exercises the complete memory system. For systems containing more than 32 K of memory, it may take several seconds before the terminal verification pattern appears. As part of the memory diagnostic, the memory configuration installed in the terminal controller is saved and can be examined if desired.
2.4.2.1 Verification Test Pattern and Diagnostics. Figure $2-6$ shows the verification test pattern that is displayed on each display indicator when the teminal controller is initialized in the LOCAL mode. The pattern remains displayed until terminated by the proper command or until approximately 45 minutes have elapsed since that last performed operation that affected the pattern.

When the system is first initialized in the LOCAL mode, ' $X X$ ' appears in the small box in the lower right portion of the pattern. The ' XX ' indicates that the code appearing in the same box contains the results of the built-in diagnostic routines that were automatically performed. The diagnostic code is a three-digit octal representation of an eight bit binary code that indicates the results of each diagnostic routine. Bits in the binary code are assigned as follows:


When a diagnostic routine detects a malfunction, the corresponding bit in the error code is set to a li if no malfunction is detected, the bit is set to a 0. The octal code displayed in the verification test pattern then tells you the results of all the diagnostic tests. For example, 000 indicates all tests passed, 002 indicates the display processor diagnostic test failed, 030 indicates the serial and the parallel interface diagnostic tests failed, and 077 indicates that all diagnostic tests failed.

As soon as the terninal controller receives any input via a serial interface port, the "XX" in the small box is replaced by a code that indicates the port to which the input device is connected. Codes associated with each serial interface port are shown in table 2-6.

Table 2-6. Serial Interface Port Codes

| CODE | SERIAL INTEREACE PORT | DEVICE | ASSOCIATED CONNECTOR |
| :---: | :---: | :---: | :---: |
| F1 | 3 | Keyboard (with function keys) | J5 on multiport serial interface cara no. 1 |
| F2 | 7 | Keyboard (with function keys) | J5 on multiport serial interface card no. 2 |
| TT | TTY | Teletypewriter | J2 on ROM and status card |
| $\begin{gathered} \text { S1, } \\ \text { S5 } \end{gathered}$ | 1 or 5 | Any | J2 or J3 on multiport serial interface card no. 1 or no. 2 |
| HC | 5 | Hardcopy | J3 on multiport serial interface card no. 2 |

NOTE
No indicator code is provided for ports 4 or 8 . These ports are normally used for PEDS which have separate indicators on the test pattern.

When the serial interface port designation is displayed in the small box, the three digit octal code in the box indicates the code last transmitted to the terminal controller. Also, if the code represents a displayable character, the character appears in the upper left corner of the box. If the code does not represent a displayable character, the upper left corner of the box is blank. In systems using SI (shift-in) and SO (shift-out) codes to identify characters in an extended set, the SI character is displayed over the left hand digit of the code and the so character is displayed over the right hand digit.

The test results box also contains a single-digit real-time clock counter readout in the lower right corner. This. counter increments from 0 through 7 continuously at a l-Hz rate to confirm operation of the real-time clock timing function.

The numeral in the upper center of the verification test pattern indicates the video controller card to which the display indicator is connected.

Trackball (or forcestick) indicators appear in the upper left corner of the verification test pattern. The "l*" indicator is associated with the device normally connected to serial interface port 4 (J6 on multiport serial interface card no. 1) while the " $2^{* *}$ is associated with the device normally connected to serial interface port 8 (J6 on multiport serial interface card no. 2). These indicators are always displayed on the test pattern regardless of whether or not a trackball or forcestick is connected to the system. If a trackball or forcestick is connected to port 4 or 8 , it can be manipulated to move its associated indicator about the screen of the CRT as desired.

PHOTOPEN indicators are displayed in the upper right corner of the verification test pattern. The "*1s" with an arrow is associated with a PHOTOPEN connected to the PPN1 connector on the front of the terminal controller. the "*2S" with an arrow is associated with a PHOTOPEN connected to the PPN2 connector. Like the trackball/ forcestick indicators, the PHOTOPEN indicators appear on the verification test pattern whether or not PHOTOPENS are connected to the system.

If a PHOTOPEN is connected to the system, its associated indicator responds as light from various data items is sensed by the PHOTOPEN. Whenever an item of data is sensed, the sensed point is intensified and the indicator moves so that the arrow points to the location at which the data item ends. Alphanumeric data is normally stored with two characters per data item. Therefore, the arrow always
points to the end of the second character in a pair. If the PHOTOPEN is also pointed at the character, an asterisk is added to the indicator. When the PHOTOPEN is pointed at the first character in a pair or at a non-character data item, the asterisk is removed from the indicator.

The "S" in each indicator provides an indication of PHOTOPEN switch operation. When you actuate the switch by pressing the PHOTOPEN against the CRT screen, the "S" is removed from the indicator. Pressing the switch a second time causes the "S" to reappear with the indicator.

## NOTE

The complete character set is displayed at the bottom center of the terminal verification pattern. In this area all characters are insensitive to PHOTOPEN strikes.

You can test the data tablet by pressing function key Fl. This causes the $1^{*}$ and $2^{*}$ trackball/forcestick indicators to change to $1 \#$ and 2\#. The $1 \#$ and $2 \#$ symbols indicate that all messages received via ports 4 and 8 are in data tablet format. (Data tablet messages consist of 10-character messages, whereas the trackball and forcestick generate 2 -character messages.) When you press the data tablet pen switch and move the pen along the active area of the data tablet surface, the appropriate cursor symbol (1\# or $2 \#$ ) moves at a rate proportional to the movement of the pen. The l\# symbol is associated with the data tablet connected to port 4 and the $2 \#$ symbol is associated with the data tablet connected to port 8.

## NOTE

Successively pressing function key FI causes the terminal verification pattern to switch from processing data tablet messages to trackball/forcestick messages and vice versa.
2.4.2.2 Local Mode Commands. After the GRAPHIC 8 has been initialized in the LOCAL mode and the verification test pattern is no longer required, you can terminate the pattern by pressing the RETURN key on the keyboard. The pattern then disappears and the letters "BO M" are displayed in the center of the CRT screen as an indication that the system is in the LOCAL MONITOR mode. At this point, you can perform any of several operations that let you monitor or debug a program, transfer control, or communicate with the host computer.

The following paragraphs discuss commands that can be executed when the system is in the LOCAL MONITOR mode in more detail. Table $2-7$ is a summary of the commands.

Commands are executed when you press the RETURN key on the standard keyboard.
a. Memory Commands. You can display the content of a memory location by typing the octal address (typing of leading zeros is not required) followed by a slash (/). As soon as you type the slash, the content of the memory location is displayed immediately to the right of the address. You can examine successive memory locations by simply pressing the slash key. Each time you press the slash key, the memory address is incremented by two and its content displayed immediately to the right of the slash.

Table 2-7. Local Mode Command Summary

| KEYBOARD ENTRY | OPERATION |
| :---: | :---: |
| RETURN | Executes LOCAL mode command or returns system to LOCAL MONITOR level. |
| nnnnnn/ | Displays contents of memory address nnnnnn (octal). |
| 1 | Increments memory address counter by two and displays address contents. |
| $\wedge$ or 1 | Decrements memory address counter by two and displays address contents. |
| Bn | Select different memory bank. (B0 0-32K; Bl 32-64K; B2 64-96K; B3 96-128K; and B4 16-32K RAM). |
| S | Transfers GRAPHIC 8 to SYSTEM mode operation |
| T RETURN | Transfers to the verification test pattern. |
| L RETURN | Loads memory from paper tape reader. |
| nnnns RETURN | Loads selected option from expansion module |
| U RETURN | Unload all options |
| - RETURN | Display status of all options loaded |
| Q | Decrements contents of display processor $Q$ register by two and displays result. Used with diagnostics to indicate address at which display processor halted. |
| nnnnnnd <br> RETURN | Directs graphic controller to display refresh file beginning at address nnnnnn (octal). |

Table 2-7. Local Mode Command Summary (Cont)

| REYBOARD ENTRY | OPERATION |
| :---: | :---: |
| nnnnnng RETURN | Transfers control of display processor to program beginning at memory address nnnnnn (octal). |
| Y RETURN | Calls teletypewriter emulation program. After entering emulation program, function key $F 0$ clears CRT screen. Function key 1 selects full or half duplex operation; receipt of octal code 035 from the host computer or pressing function key Fl3 transfers GRAPHIC 8 to SYSTEM operating mode. |
| RUB OUT | Deletes last octal entry from keyboard. |

After you have used the slash key to examine the content of a memory location, you can use the up arrow ( $\wedge$ or $\mid$ ) key in a similar manner to examine preceding memory locations. Each time you press the up arrow key, the memory address is decremented by two and its content displayed immediately to the right of the slash.

You can change the content of a memory location after you have examined it by typing the new data (typing of leading zeros is not required) before pressing the slash or up arrow key. The new data is displayed to the right of the old data and is automatically substituted when the slash or up arrow key is pressed.

You can examine or change memory locations in other banks via the bank ( $B$ ) select command. Typing B0, B1, B2, B3, or B4 changes the memory bank selection to bank 0 , bank 1 , bank 2 , bank 3 , or bank 4 respectively. Below is a table representing the associated virtual and physical addresses for each bank.

Bank Number

| $0 *$ | $000000-177777$ |
| :--- | :--- |
| 1 | $000000-177777$ |
| 2 | $000000-177777$ |
| 3 | $000000-177777$ |
| $4^{*}$ | $100000-177777$ |

Physical Address
000000-177777
200000-377777
400000-577777
600000-777777
100000-177777

Pages
00-07
10-17
20-27
30-37
04-07

NOTE
*Addresses in the range of 100000-177777 (pages 4, 5, 6, and 7) for bank 0 correspond to ROM and I/O device registers. Addresses in the range of 100000-177777 for bank 4 correspond to RAM.
completed and the system returns to LOCAL MONITOR level as indicated by letters "BO M" displayed at the center of the CRT screen.
b. Displaying a Refresh File. When the system is at the LOCAL MONITOR level, you can display the contents of a refresh file by typing the starting address of the file (in octal notation) followed by a "D" and then pressing the RETURN key. This command instructs the digital graphic controller to display the entire refresh file that begins at the specified address. Display of the refresh file continues until you press RETURN key again, at which time the system returns to the LOCAL MONITOR level. This command is subject to the bank argument presently displayed.
c. Transfer of Program Control. You can transfer program control from LOCAL MONITOR level to any desired address location in bank 0 by typing the address location in octal notation followed by a "G" and then pressing the RETURN key. The display processor then executes instructions beginning with the instruction at the specified address. Any further operations depend on the program in which control is transferred.
d. Transfer to System Mode. To transfer to the SYSTEM mode of operation from the LOCAL MONITOR level, type "S". This command has the same effect as pressing the RUN/SYS pushbutton on the terminal controller. After transferring to the SYSTEM mode, operation in the LOCAL mode can be reestablished only by a message from the host computer, or by pressing the DIS/LOC pushbutton on the terminal controller, or by pressing CONTROL and SHIFT and RETURN on the keyboard.
e. Teletypewriter Emulation. For purposes of communicating with a host computer, the GRAPHIC 8 can be made to emulate the functions of a teletypewriter. In this mode, the keyboard operates like the keyboard of a teletypewriter and the display indicator serves as the printout device. Scrolling of data on the display indicator is handled on a half-page basis. That is, when the CRT screen is full, the top half of the data is deleted from the display and the bottom half of the data moves up to take its place.

If a parallel interface card is installed in the terminal. controller, the graphic control program assumes that communications with the host computer are to be handled over the parallel interface. In this case, teletypewriter emulation signals are transmitted in parallel using only the low order byte (bits 0-7) of the 16-bit interface. If a parallel interface card is not installed, a standard 8 -bit serial interface via serial interface port 1 is assumed. In either case, bit 7 is always equal to zero.

You enter the emulation program from the LOCAL MONITOR level by typing the letter "Y" followed by RETURN. Full-duplex or half-duplex emulation may then be selected by pressing function key Fl, which
changes the selection each time it is pressed. The type of emulation selected is indicated by the "TTY F" (full duplex) or "TTY H" (half duplex) that is displayed at the top of the CRT screen at all times during emulation. You can switch between full and half duplex operation at any time during emulation by pressing function key El. pressing function key $F 0$ during teletypewriter emulation clears the CRT screen.

Exit from the teletypewriter emulation program occurs when octal code 035 (ASCII control character GS Group Separator) is received from the host computer. This code, which can also be generated by pressing function key F13, immediately causes the GRAPHIC 8 to transfer to the SYSTEM mode of operation. On basic series 8000 systems, return to the LOCAL MONITOR level can be achieved only by a command from the host computer, or by pressing the DIS/LOC pushbutton on the terminal controller, or by pressing CONTROL and SHIFT and RETURN on the keyboard.
E. Additional Local Mode Commands. Additional commands that you can use when the GRAPHIC 8 is in the LOCAL mode at the MONITOR level are the $L, U, O, T, Q, H$, and RUB OUT commands. The $L$ command enables the memory to be loaded from a paper tape reader connected to the terminal controller. After the tape has been placed in the reader, loading is initiated by typing the letter "L" followed by REIURN.

NOTE
A paper tape reader may be connected to multiport serial interface card ports 1 , 2 , or 3 or to the serial interface port on the ROM and status logic card.

You also use the $L$ command to load in options from the expansion module. The option command format is as follows:
nnnni RETURN
where nnnn is the option number. Valid option numbers are in the ranges of 1 to 3777 and 4001 to 7777 .

NOTE
The optional expansion module can store a variety of option types.

The $U$ command is used to unload all options. Typing "U" followed by RETURN unloads all options.

The 0 command is used to detect the presence and status of all loaded options. Typing 0 followed by RETURN causes the display of the first option loaded. Successively pressing the RETURN key causes the display of all other options loaded. The option status is displayed in the following format.

Where nnnn is the option number and $s s$ is the option status code.
The option status code is as follows:

| 00 | Detected but unloaded |
| :--- | :--- |
| 01 | Unloaded, checksum error (local) |
| 11 | Unloaded, checksum error (system) |
| 02 | Unloaded, checksum OK, hardware not present (LOCAL) |
| 12 | Unloaded, checksum OK, hardware not present (SYSTEM) |
| 03 | Unloaded, checksum OR, self-test = NO GO (LOCAL) |
| 13 | Unloaded, checksum OK, self-test = NO GO (SYSTEM) |
| 04 | Loaded, checksum OK, self-test $=$ GO (LOCAL) |
| 14 | Loaded, checksum OR, self-test $=$ GO (SYSTEM) |

You use the $T$ command to recall the verification test pattern when the system is at the LOCAL MONITOR level. This command is executed by typing the letter "T" followed by RETURN. The effect is the same as pressing the DIS/LOC pushbutton on the terminal controller. Pressing RETURN a second time causes the system to return to the LOCAL MONITOR level.

The $Q$ command is a special command used for diagnostic and debugging purposes. Whenever a HALT instruction is executed by the display processor, the content of the program counter is stored in the $Q$ register of the display processor. After you have reinitialized the system by pressing the DIS/LOC pushbutton on the terminal controller, you can use the $Q$ command to display the address at which the display processor halted. The $Q$ command is executed by typing the letter "Q". This causes the content of the $Q$ register to be decremented by two and the result displayed to indicate the address of the HALT instruction. Note that the $Q$ command always decrements the content of the $Q$ register by two and displays the result. The result, howevr, is only meaningful immediately following initialization in the LOCAL mode after a HALT instruction has been executed. After using a $Q$ command, pressing RETURN returns the system to the LOCAL MONITOR level.

The RUB OUT command provides a means of correcting erroneous entries from the keyboard. At any time before a command is executed, pressing RUB OUT causes the last keystroke entry to be deleted. An additional entry is deleted each time the RUB OUT key is pressed.
2.4.3 OPERATOR PERFORMANCE CHECKS. If you suspect a malfunction in the terminal controller or any of its peripherals (or if the host computer reports the terminal controllaz to be defective), you can perform the following procedure to verify basic system operation. If you do not get the described results, refer to section 4 .

1. Press the DIS/LOC pushbutton to initialize LOCAL mode. Confirm that the verification test pattern (figure 2-6) is present on the display indicator.
2. Confirm that the test results box shows a 000 value (code for all tests passed), with the "XX" error word code displayed in the upper right corner of the box.
3. Confirm that the seconds counter at the lower right corner of the test results box runs continuously through an 8-count cycle $(0-7)$.
4. Confirm that the letter "q" appears in the upper left corner of the test results box.
5. Press one or more alphanumeric keys on the keyboard. Confirm that:
a. The alphanumeric symbol replaces the letter "T ${ }^{\text {In }}$ in the upper left corner of the test results box.
b. The applicable ASCII value in octal format replaces the test results value below that symbol.
c. The appropriate source code (table $2-6$ ) replaces the "xx" code.
6. Confirm that a full library of alphanumeric and special symbols is displayed at the lower center of the verification test pattern.
7. Confirm that three different sized sets of "EM" letter pairs are displayed in the lower left quadrant of the verification test pattern.
8. Confirm that the line structure of the overall test pattern conforms with the pattern shown in figure $2-6$ with respect to the following:
a. The corners of the displayed squares are clearly defined right angles, with uniform intensity through the points of congruence in all four corners.
b. The central portions of the sides of the inner square represent different line structure patterns: solid (bottom), dotted (right), dashed (left), and centerline (top).
c. The circle and ellipse patterns are smooth and unbroken; the dotted cross and arc segments are as shown in figure 2-6.
d. The triangle is smooth and complete, as shown in figure 2-6.
9. Confirm that the number of the video controller card associated with a specific display indicator appears in the upper center of the pattern, continuousiy blinking on and off.
10. Confirm that the trackball/forcestick/data tablet identifier symbols are initially presented in the upper left corner of the pattern, with the applicable symbol moving as appropriate for any PED displacement inputs.
11. Confirm that the PHOTOPEN identifier signals are initially presented in the upper right corner of the test pattern with the applicable symbol moving as appropriate for any strike inputs, and responding appropriate ("S" character disappearing or reappearing) each time you press the PHOTOPEN switch.
12. In the case of a four-color display, confirm that the proper colors are displayed.
2.4.4 HARD COPY USAGE. Both the monochrome hard copy unit and the color hard copy unit produce high contrast paper copies from standard, raster scan signals of refreshed graphics terminals. The following paragraphs describe how to make a hard copy of any displayed pattern using each of these units.
2.4.4.1 Monochrome Hard Copy Unit. To make a black-and-white or gray-scale hard copy of a displayed pattern using a monochrome hard copy unit, proceed as follows:
13. Press function key $F 0$ on the keyboard; this action freezes the display.
14. Press the COPY pushbutton on the hard copy unit once for each copy required. Adjust the LIGHT/DARK control as needed for best copy quality.
15. Press function key $F 0$ on the keyboard again to allow the display to update.
2.4.4.2 Color Hard Copy Unit. To make an $8 \times 10$ instant color print of a displayed pattern, proceed as follows:
16. Press function key $F O$ on the keyboard; this action freezes the display.
17. Set the color hard copy unit mode select lever to the $8 \times 10$ position.
18. Set the front control panel NORMAL/SEPARATE switch to NORMAL.
19. Set the front control panel COLOR/FOCUS switch to COLOR.
20. . Press the front control panel RESET pushbutton; wait for the green indicator lamp in the EXPOSE pushbutton to light.
21. Grasp the blue handles on the dark slide in the film cassette and pull the slide out as far as it will go.
22. Press the front control panel EXPOSE pushbutton. Each of the SEQUENCE indicator lamps should light in turn as the three exposures are made: the red exposure should last approximately 40 seconds, the green exposure seven seconds, and the blue exposure three seconds. At the end of the exposure sequence, the color hard copy unit speaker should beep.
23. Push the dark slide on the film cassette all the way in.
24. Process the film. Refer to the instruction sheet supplied with the box of film and the instruction book supplied with the film processor for details.

To make additional $8 \times 10$ instant color prints, you must first reload the film cassette and reinsert it in the color hard copy unit. (Refer to the instruction sheet supplied with the box of film for loading procedure.) However, you do not need to press the front control panel RESET pushbutton before making each additional print. Simply repeat steps 6 through 9 above. Use function key $F 0$ on the keyboard to update and freeze the display, as desired, before making each new exposure.

To make a 35 mm slide of a displayed pattern, proceed as follows:

1. Press function key $F O$ on the keyboard to freeze the display.
2. Set the color hard copy unit mode select lever to the AUX position.
3. Set the front control panel NORMAL/SEPARATE switch to NORMAL.
4. Set the front control panel COLOR/FOCUS switch to COLOR.
5. Press the front control panel RESET pushbutton; wait for the green indicator lamp in the EXPOSE pushbutton to light.
6. Press the front control panel EXPOSE pushbutton. Each of the SEQUENCE indicator lamps should light in turn as the three exposures are made; the red exposure should last approximately 40 seconds, the green exposure seven seconds, and the blue exposure three seconds. At the end of the exposure sequence, the color hard copy unit speaker should beep.

You may continue making 35 mm exposures simply by pressing the front control panel EXPOSE pushbutton. You do not need to press the RESET pushbutton before making each additional exposure. Use function key $E 0$ on the keyboard to update and freeze the display, as desired, before each exposure. When you reach the end of the roll of film, the color hard copy unit will:

- Go into the red exposure cycle (the red SEQUENCE indicator lamp should light and remain lit)
- Terminate the 35 mm shutter
- Beep three times
- Light the front control panel OUT OF FILM indicator lamp To remove the exposed roll of film, proceed as follows:

1. Move the lever marked $R$ on the real-time winder toward the camera.
2. Fold out the film rewind crank handle and turn it clockwise. Keep turning until the crank handle rotates freely.
3. Open the auxiliary camera back cover and take out the roll of film.

Process the roll according to the directions given on the instruction sheet supplied with the film.
2.4.5 PHOTOPEN USAGE. TO Operate the model 5781 PHOTOPEN, proceed as follows:

1. Locate the desired target (character, symbol, line, or other display element) on the display screen. The display target area must be lighted.
2. Place the tapered front end of the PHOTOPEN against the display screen, pointed directly at the target location.
3. Press downward on the PHOTOPEN, sliding the outer shell forward. This action activates the PHOTOPEN ON switch. The termninal controller responds according to the software instructions supplied with the GRAPHIC 8 system. Hold the outer shell forward as long as necessary.
4. To turn the PHOTOPEN off, release the downward pressure on the outer shell and allow it to return to its normal position.
2.4.6 TRACKBALL/FORCESTICK USAGE. To operate the model 5786 trackball, rotate the trackball in the direction in which the cursor or other displayed data is to move on the display screen. Rotate the trackball faster to increase the speed at which the data moves across the screen, or slower to decrease the speed.

To operate the model 5787 forcestick, push the control rod in the direction in which the cursor or other displayed data is to move across the display screen. Increase the pressure on the control rod to increase the speed at which the data moves across the screen. Release the control rod to stop the movement.
2.4.7 DATA TABLET USAGE. The model 5788 data tablet may be operated in either the POINT mode or the RUN mode. To select the POINT mode, set the operation mode select switch, located on the data tablet rear panel, to position A. To select the RUN mode, set the operation mode select switch to position $B$.

The POINT mode is used for entering. charts and graphs or for making point-to-point plots and measurements. To operate the data tablet in the point mode, proceed as follows:

1. Place the material to be digitized on the active surface of the data tablet.
2. Place the point of the pen stylus over the place on the material where the first coordinate to be plotted appears.
3. Lift the pen stylus from the active surface of the data tablet and place it over the place where the next coordinate appears.
4. Lift the pen stylus from the active surface of the data tablet again before entering each additional coordinate. For best results, hold the pen stylus perpendicular to the active surface of the data tablet.

The RUN mode is used for creating graphics such as maps, stylized Ine drawings, and special symbols. To operate the data tablet in the RUN mode, proceed as follows:

1. Place the material to be digitized on the active surface of the data tablet.
2. Hold the pen stylus within one-half inch above the active surface of the data tablet.
3. Move the pen stylus over the material to be aigitized, using a continuous motion. The data tablet will transmit a continuous stream of data identifying the immediate location of the pen stylus to the terminal controller for display.
2.4.8 TURN-OFF PROCEDURES. To turn off the terminal controller, press the 0 side of the POWER ON/OFF circuit breaker.

To turn off the color monitor, set the POWER ON/OFF switch to OFF.

To turn off the monochrome monitor, press the POWER switch.
To turn off the monochrome hard copy unit, set the POWER OFF/ON switch (on top of the unit) to OFF.

## SECTION

## INSTALLATION

### 3.1 ENVIRONMENTAL CONSIDERATIONS

A typical GRAPHIC 8 system is designed to operate in an ambient temperature range of $+15^{\circ} \mathrm{C}$ to $+40^{\circ} \mathrm{C}\left(+59^{\circ} \mathrm{F}\right.$ to $\left.+104^{\circ} \mathrm{F}\right)$, with a relative humidity not exceeding 90 percent, and at altitudes not exceeding 3048 meters ( 10,000 feet). However, the use of optional equipment components (such as a color hard-copy unit) may restrict these ranges. Refer to the appropriate table in Section 1 for the environmental considerations that apply to any optional component included as part of your GRAPHIC 8 system.

The GRAFHIC 8 terminal controller, color monitor, and monochrome monitor can be mounted in EIA standard 19-inch equipment racks, either directly or on optional slide assemblies. The terminal controller card cage (standard or extended) occupies 10.5 inches of rack height. The low voltage power supply (LVPS) and control panel used with the optional, extended card cage occupies an additional 8.72 inches of rack height. A GRAPHIC 8 color monitor typically occupies 14.4 to 17.4 inches of rack height, depending on the model. A GRAPHIC 8 monochrame monitor occupies 15.7 inches of rack height.

The teminal controller can also be mounted in an equipment cabinet (Sanders part number 5976104). The equipment cabinet also accommodates a system interconnect panel and a power panel assembly. The system interconnect panel occupies a 9.25-inch vertical space; the power panel assembly occupies a 10.48-inch vertical space. Both the system interconnect panel and the power panel assembly may also be rack mounted.

### 3.2 EQUIPMENT CABINET

The equipment cabinet is a four-wheeled, semi-portable equipment rack with the following approximate dimensions:

| Height | Width | Depth |
| :---: | :---: | :---: |
| 30 inches | 23 inches | 30 inches |
| $(76.3 \mathrm{~cm})$ | $(58.4 \mathrm{~cm})$ | $(76.2 \mathrm{~cm})$ |

The cabinet has doors on both front and back. The front door is hinged on its right side, and is held shut by a magnetic latch. The front door has a cut-out to give access to the teminal controller controls and indicators. The rear door is hinged on its left side, and is held shut by a magnetic latch.

When the equipment cabinet contains the standard terminal controller, system interconnect panel, and power panel assembly, they are arranged as follows:

1. The teminal controller is accessible from the front of the cabinet, and is installed in the upper half of the cabinet.
2. The power panel assembly is accessible from the front of the cabinet, and is installed in the lower half of the cabinet.
3. The system interconnect panel is accessible from the rear of the cabinet, and is installed in the lower half of the cabinet.

When the equipment cabinet contains the extended teminal controller, LVPS and control panel, system interconnect panel, and power panel assembly, they are arranged as follows:

1. The LVPS and control panel is accessible from the front of the cabinet, and is installed in the upper half of the cabinet.
2. The teminal controller is accessible from the front of the cabinet, and is installed in the lower half of the cabinet.
3. The system interconnect panel is accessible from the rear of the cabinet, and is installed in the upper half of the cabinet.
4. The power panel assembly is accessible from the rear of the cabinet, and is installed in the lower half of the cabinet.

A cut-out in the bottom of the cabinet, below the system interconnect panel, is the entryway for power and signal cables. There is also an air filter in the bottom of the cabinet. Refer to section 4 for access and maintenance instructions.

### 3.3 PONER PANEL ASSEMBLY

3.3.1 STANDARD POWER PANEL ASSEMBLY. The standard power panel assembly is sanders part number 5976122 . This power panel assembly is usable with prime power voltages of $100 \mathrm{Vac}, 120 \mathrm{Vac}, 220 \mathrm{Vac}$, and 240 vac.

The power panel assembly contains a circuit breaker (CBI). in the prime power lines; a programable power transformer; a line filter assembly; a l5-pin connector (J3) for voltage configuration; a power contactor (K1); and a duplex 110 Vac power outlet (J1, J2).

Outlet $J 1$ is live when the circuit breaker on the power panel assembly is ON. Outlet $J 2$ is live when the circuit breaker is $O N$ and relay Kl is energized. Relay Kl is energized by a control signal at connector P2 on the power panel.

The power cord is Belden type 17612 (length 6 feet 7 inches, or 2 meters). If the primary power source is not $120 \mathrm{~V}, 60 \mathrm{~Hz}$, the first step in installation is to connect an appropriate power connector to this power cord. The three lines in the power cord are color-coded as follows:

Light blue for the neutral line
Brown for the high line
Green/yellow for safety ground
The power cord is soldered to terminals on the line filter. The line filter suppresses transients that may appear on the primary power line. The output of the line filter goes to the circuit breaker.

The circuit breaker opens if the current at 115 V exceeds 10 A or if the current at 220 V exceeds 5 A .

The high line output of the circuit breaker goes to pin 1 of connector J3 (brown wire) and to pin 3 of connector J3 (light blue wire). The neutral line output of the circuit breaker goes to pin 2 of connector $J 3$ (tan wire) and to pin 4 of connector J3 (dark blue wire).

The mating connector $P 3$ contains jumper connections that set up the primary windings of the power transformer to match the input voltage. Connector P3 is wired as shown in table 3-1.

Table 3-1. Connector P3 Configurations

| G-CONDITI | INPUT VOLTAGE | JUMPERS INSTALIED |
| :---: | :---: | :---: |
| GI | 100 Vac | $\text { I to 6; } 3 \text { to } 7 \text {; } 10 \text { to } 13 \text { to } 15 \text {; }$ $11 \text { to } 12 \text { to } 14$ |
| G2 | 120 Vac | 1 to 5 to 8; 3 to 7 to 9; 10 to 13 to 15 ; 11 to 12 to 14 |
| G3 | 220.Vac | 2 to 6; 4 to 9; 7 to 8; 10 to 13 to 15; 11 to 12 to 14 |
| G4 | 240 Vac | 2 to 5; 4 to 9; 7 to 8; 10 to 13 to 15: 11 to 12 to 14 |

Connections from J3 to the transformer primary winding are as follows:

From J3-5 to Tl-C (white)
From J3-6 to Tl-A (white)
Fram J3-7 to Tl-E (white)
From J3-8 to Tl-D (white)
Fran J3-9 to Tl-F (white)
The output from the transfomer secondary winding (110 Vac) goes back to connector J3 as follows:

```
From Tl-1 to J3-10 (light blue)
From Tl-2 to J3-11 (brown)
```

The 110 Vac lines go from connector $J 3$ to the duplex output box connector Jl as follows:

```
Fram J3-13 to JI-N (light blue)
From J3-12 to J1-I (brown)
```

The ground pin of the duplex output box is connected to the outlet box mounting stud.

The 110 Vac lines go from connector J3 to the power contactor R1 as follows:

```
From J3-15 to Rl-8 (light blue)
Fram J3-14 to KI-7 (brown)
```

The 110 Vac lines go from power contactor Kl to duplex output box connector J 2 as follows:

From KI-4 to $\mathrm{J} 2-\mathrm{N}$ (light blue)
From R1-3 to J2-L (brown)
The power contactor control signal goes from connector P2 to power contactor kl as follows:

```
From P2-1 to Kl-5 (red)
From P2-3 to Kl-6 (black)
From P2-2 to Kl mounting stud (shield)
```

3.3.2 ALTERNATIVE POWER PANEL ASSEMBLY. The alternative power panel assembly (Sanders part number 5976121 ) is usable if the prime power is 110 Vac . The alternative power panel contains a filter, a circuit breaker, a relay, and a duplex outlet box. The power cord is Belden type 17612 with the original power connector left on.

Outlet Jl is live when the circuit breaker on the power panel assembly is $O N$. Outlet $J 2$ is live when the circuit breaker is $O N$ and power contactor Kl is energized. The power contactor is energized by a control signal at connector P2 on the power panel.

### 3.4 SYSTEM INTERCONNECT PANEL ASSEMBLY

The system interconnect panel (Sanders part number 597610.5) provides a convenient means of connecting the terminal controller to the host camputer, to the displays, and to other peripheral devices (keyboards, position entry devices, PHOTOPENs).

The panel assembly consists of a panel with cutouts and a number of cable assemblies with connectors; the connectors are secured to the panel at their respective cutouts.

Figure $3-1$ shows typical wiring between the terminal controller and the system interconnect panel and lists the part numbers of the cable assemblies. Note that the figure shows the back side of the interconnect panel assembly. The figure shows specifically the standard card cage teminal controller. The slot numbers of the individual circuit cards differ from the standard card cage to the extended card cage; refer to figure $1-7$ for extended card cage card locations.



### 3.5 CONNECTIONS TO BE MADE AT INSTALLATION

3.5.1 TERMINAL CONTROLLER MOUNTED IN EQUIPMENT CABINET. If the teminal controller and equipment cabinet are ordered at the same time, the teminal controller is shipped from the factory installed in the equipment cabinet, and all connections between the terminal controller and the system interconnect panel are already made.

For other situations, refer to figure 3-1.
Connections between the system interconnect panel and other devices are a function of the individual installation. Refer to the top assembly drawing for your installation. In general:

1. Connections to the host computer are made by cables from JI and $J 8$ of the system interconnect panel to the appropriate point in the host computer. If the parallel interface is not Sanders model no. 5712 (part number 1086802), then the cables needed to connect between the system interconnect panel and the host computer. may be supplied with the parallel interface. If the parallel interface is Sanders model no. 5712, then the interconnecting cables are the customer's responsibility.
2. If the GRAPHIC 8 system does not contain a parallel interface, and communications with the host computer are through the multiport serial interface, then the cable needed to connect between the system interconnect panel and the host computer is the customer's responsibility. The cable would normally be connected to the $J 2$ connector on the system interconnect panel, but in some cases could be connected to J3.
3. If the GRAPHIC 8 system includes one alphanumeric keyboard, the keyboard connects to $J 5$ on the system interconnect panel. However, if the associated display indicator pedestal contains an accessory panel, the keyboard may plug into that accessory panel. Another cable leads from the accessory panel to $J 5$ on the system interconnect panel.
4. If the GRAPHIC 8 system includes two alphanumeric keyboards and two serial interface cards, the second keyboard connects (either directly or through an accessory panel) to Jl2 on the system interconnect panel.
5. If the GRAPHIC 8 system includes two alphanumeric keyboards and only one serial interface card, the second keyboard connects (either directly or through an accessory panel) to $J 4$ on the system interconnect panel; the serial interface card must be modified to make its port 2 appear to be port 7.
6. If the GRAPHIC 8 system includes one position entry device (trackball, forcestick, or data tablet), the PED connects to J6 on the system interconnect panel. However, if the associated display indicator pedestal contains an accessory panel, the PED may plug into that accessory panel. Another cable leads from the accessory panel to $J 6$ on the system interconnect panel.
7. If the GRAPHIC 8 system includes two PEDs and two serial interface caras, the second PED connects (either directly or through an accessory panel) to $J 13$ on the system interconnect panel.
8. If the GRAPHIC 8 system includes two PEDs and only one serial interface card, the second PED connects (either directly or through an accessory panel) to $J 4$ on the system interconnect panel; the serial interface card must be modified to make its port 2 appear to be port 8 .
9. If the GRAPHIC 8 system includes one PHOTOPEN, it connects to J26 on the system interconnect panel. However, if the associated display indicator pedestal contains an accessory panel, the PHOTOPEN may plug into that accessory panel. Another cable leads from the accessory panel to $J 26$ on the system interconnect panel.
10. If the GRAPHIC 8 system includes two PHOTOPENs, the second one connects (either directly or through an accessory panel) to $J 27$ on the system interconnect panel. In this case the customer must specify an additional cable, part number 5976150 Gl , to connect from the system interconnect panel to the PHOTOPEN 2 connector on the teminal controller.
11. For all color systems, connectors J14 through J16 on the system interconnect panel are exact replicas of connectors J3, J5, J7 of the video controller card \#l. Similarly Ji7 through $J 19$ on the system interconnect panel correspond to J3, J5, J7 of video controller card \#2; J20 through J22 pertain to video controller card \#3, and 323 through J25 pertain to video controller card \#4. For systems with monochrome applications, connector $J 2$ on the respective video controller card connects to J16, J19, J22, or J25 on the system interconnect panel.
12. Connector $J 2$ on the ROM and status card (usable with a teletypewriter, paper tape reader, keyboard, PED, or even the host computer) does not have a camparable connector on the system interconnect panel. Any connection must be made directly to the connector on the edge of the ROM and status card, using a cable with suitable connectors.
3.5.2 TERMINAL CONTROLLER WITHOUT EQUIPMENT CABINET. If the teminal controller is purchased without the equipment cabinet (as for rack mounting or installation in a display console), and if the system
interconnect panel is not procured，then connections between the terminal controller，the host computer，and the peripheral devices must be made directly to connectors on the terminal controller．

If the GRAPHIC 8 system includes a parallel interface，the cables from the host computer go to connectors $J 2$ and J3 on the edge of the parallel interface card．J2 accepts output data and control signals from the host computer．J3 carries input data and control signals to the host computer．Table 3－2 lists and identifies the pins in parallel interface connectors J 2 and J 3 for the model 5712 parallel interface．

Table 3－2．Parallel Interface I／O Connectors，Pin Assignments

| JACR／PIN | SIGNAL | JACK／PIN | SIGNAL | JACK／PIN | SIGNAL |
| :---: | :---: | :---: | :---: | :---: | :---: |
| J2－1 | 0000（土） | J2－20 | DRET－ | J3－39 | $\operatorname{OMR}( \pm)$ |
| J $2-2$ | DRET－ | J2－21 | OD10（ $\pm$ ） | J3－40 | DRET－ |
| J 2 －3 | ODO1（ $\pm$ ） | J2－22 | DRET－ | J3－41 | OCTL（ $\pm$ ） |
| J 2－4 | DRET－ | J2－23 | 0011（ $\ddagger$ | J3－42 | DRET－ |
| J $2=5$ | 0002 $2 \pm$ ） | J2－24 | DRET－ | J3－43 | ODR（ $\pm$ ） |
| Ј 2－6 | DRET－ | J2－25 | 0012（ $\pm$ ） | J3－44 | DRET－ |
| Ј 2－7 | 0003（ $\pm$ ） | J2－26 | DRET－ | J3－45 | SPARE |
| J 2－8 | DRET－ | J2－27 | 0013（土） | J3－46 | SPARE |
| J2－9 | 0004（ $\pm$ ） | J2－28 | DRET－ | J3－47 | INIT |
| J2－10 | DRET－ | J2－29 | OD14（土） | J3－48 | DRET－ |
| J2－11 | 0005（土） | J2－30 | DRET－ | J3－49 | ＊IMR（ $\pm$ ） |
| J2－12 | DRET－ | J2－31 | 0015（ $\pm$ ） | J3－50 | SPARE |
| J2－13 | 0006（土） | J2－32 | DRET－ |  |  |
| J2－14 | DRET－ | J2－33 | ＊SPR1－ | J3－1 | IDOO（土） |
| J 2－15 | $0007( \pm)$ | J2－34 | SPARE | J3－2 | DRET－ |
| J2－16 | DRET－ | J2－35 | ATNI（ $\pm$ ） | J3－3 | IDO1（ $\pm$ ） |
| J2－17 | 0008（土） | J2－36 | DRET－ | J3－4 | DRET－ |

＊Signals used only in test operation using input to output（J3 to J2） loop cable．

Table 3-2. Parallel Interface I/O Connectors, Pin Assignments (Cont)

| JACK/PIN | SIGNAL | JACR/PIN | SIGNAL | JACK/PIN | SIGNAL |
| :---: | :---: | :---: | :---: | :---: | :---: |
| J2-18 | DRET - | J2-37 | OWR( $\pm$ ) | J3-5 | IDO2 $2 \pm$ ) |
| J2-19 | $0009( \pm)$ | J2-38 | DRET | J3-6 | DRET- |
| J3-7 | IDO3( $\pm$ | J3-22 | DRET- | J3-37 | $\operatorname{ICTL}( \pm)$ |
| J3-8 | DRET- | J3-23 | IDII( $\pm$ ) | J3-38 | DRET- |
| J3-9 | TDO4 (t) | J3-24 | DRET- | J3-39 | $\operatorname{ATN} 2( \pm)$ |
| J3-10 | DRET- | J3-25 | ID12(t) | J3-40 | DRET- |
| J3-11 | ID05 (土) | J3-26 | DRET- | J3-41 | $\operatorname{IWR}( \pm)$ |
| J3-12 | DRET- | J3-27 | IDI3( $\pm$ ) | J3-42 | DRET- |
| J3-13 | ID06 ( $\pm$ ) | J3-28 | DRET- | J3-43 | SPARE |
| J3-14 | DRET- | J3-29 | IDI4( $\pm$ ) | J3-44 | DRET- |
| J3-15 | ID07 ( $\pm$ | J3-30 | DRET- | J3-45 | $\operatorname{NDRY}( \pm)$ |
| J3-16 | DRET-- | J3-31 | ID15 ( + ) | J3-46 | SPARE |
| J3-17 | ID08 ( $\pm$ ) | J3-32 | DRET- | J3-47 | SPARE |
| J3-18 | DRET- | J3-33 | *SPR1- | J3-48 | SPARE |
| J3-19 | ID09 ( $\ddagger$ ) | J3-34 | SPARE | J3-49 | *SPR2- |
| J3-20 | DRET- | J3-35 | $\operatorname{IMR}( \pm)$ | J3-50 | SPARE |
| J3-21 | IDIO( + ) | J3-36 | DRET- |  |  |

*Signals used only in test operation using input to output (J3 to J2) loop cable.

If the GRAPHIC 8 system does not include a parallel interface, the cable from the host computer goes to connectors J2 or J3 on the edge of the serial interface card, or (in some cases) to connector J2 on the ROM and status card. In addition, peripheral devices (keyboard, PED) connect directly to the appropriate connector on the edge of the serial interface card. It is imperative that the serial interface card be configured to match each port to the type of device connected to it. Table 3-3 lists and identifies the pins in serial interface connectors $J 2$ through J6.

Table 3-3. Multiport Serial Interface I/O Connectors, Pin Assignments

| JACK/PIN | SIGNAL |  | JACK/PIN | SIGNAL |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| J2-1 | CGND | PORT 1 | J2-25 | SPARE |  |
| J 2 -2 | SPARE |  | J2-26 | SPARE |  |
| J $2-3$ | 8232- |  |  |  |  |
| J2-4 | TSCK+ |  | J3-1 | XMT1- | PORT 1 |
| J 2-5 | RDA1- |  | J3-2 | $\mathrm{P} 15 \mathrm{~V}+$ |  |
| Ј 2-6 | SPARE |  | J 3-3 | DRET- |  |
| J 2-7 | ROTS + |  | J 3-4 | DRET- |  |
| Ј 2-8 | RSCK+ |  | J 3-5 | RDAl- |  |
| J 2-9 | CLTS + |  | J 3-6 | P05V+ |  |
| J2-10 | SPARE |  | J3-7 | DRET- |  |
| J2-11 | DSRY+ |  | J 3-8 | DRET- |  |
| J2-12 | SPARE |  | J3-9 | REN1- | PORT 1 |
| J2-13 | SPARE |  | J3-10 | N15V |  |
| J2-14 | DGND |  |  |  |  |
| J2-15 | DTRY ${ }^{+}$ |  | J 4-1 | XMT2- | PORT 2 |
| J2-16 | CARR+ |  | J 4 -2 | P15V+ |  |
| J2-17 | SPARE |  | J 4-3 | DRET- |  |
| J2-18 | SPARE |  | J 4-4 | DRET- |  |
| J2-19 | RING+ |  | J 4-5 | RDA2- |  |
| J2-20 | SPARE |  | J 4-6 | P0 5V + |  |
| J2-21 | SpARE |  | J 4-7 | DRET- |  |
| J 2-22 | SPARE |  | J 4-8 | DRET- |  |
| J2-23 | TXCO+ |  | J 4-9 | REN2- |  |
| J2-24 | SPARE |  | J 4-10 | N1 5V- |  |

Table 3-3. Multiport Serial Interface I/O Connectors, Pin Assignments (Cont)

| JACR/PIN | SIGNAL |  | JACK/PIN | SIGNAL |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| J5-1 | XMT3E | PORT 3 | J6-1 | XMT4- | PORT 4 |
| J5-2 | P15V+ |  | J6-2 | P15V+ |  |
| J5-3 | DRET- |  | J6-3 | DRET- |  |
| J5-4 | DRET- |  | J6-4 | DRET- |  |
| J5-5 | RDA3- |  | J6-5 | RDA4- |  |
| J5-6 | P0 5V- |  | J6-6 | P05V+. |  |
| J5-7 | DRET- |  | J6-7 | DRET- |  |
| J5-8 | DRET- |  | J6-8 | DRET- |  |
| J5-9 | REN3- |  | J6-9 | REN4- |  |
| J5-10 | N15V- |  | J6-10 | N15V- |  |

The J 2 connector on the ROM and status card is identical to connectors $J 3$ through $J 6$ of the serial interface.

Display indicators connect to $\mathrm{J} 3, \mathrm{~J} 5, \mathrm{~J} 7$ on the edge of the video controller card. Refer to the technical manual for your display indicator for the method of making connections.

PHOTOPENs connect directly to PHOTOPEN connector $J 1$ and $J 2$ on the terminal controller card cage.
3.5.3 MONITOR CONNECTIONS. The monitor power cables may be connected to primary power via convenience outlets furnished at the installation site, or via convenience outlets on the GRAPHIC 8 system power panel (if installed). For proper operation, the monitors must be connected to the same phase of primary power as the teminal controller. The following paragraphs describe the signal connections for the color and monochrome display monitors.
3.5.3.1 Color Monitor. Each color display monitor in a GRAPHIC 8 system requires three video inputs for proper operation: R=red, $G=g r e e n$, and $B=b l u e$. Typically, a dual coaxial cable carries the monitor $R$ and $G$ inputs and a single coaxial cable carries the monitor $B$ input. The dual coaxial cable connects to connectors J3 and J5 on the corresponding video controller card in the terminal controller. The single coaxial cable connects to connector $J 7$ on the video controller card. If the GRAPHIC 8 system includes a system interconnect panel, the $R$ and $G$ video signals may be brought out via
connectors J14 and J15, J17 and J18, J20 and J21, or J23 and J24 on the panel. Similarly, the $B$ video signal may be brought out via connector $\mathrm{J} 16, \mathrm{~J} 19, \mathrm{~J} 22$, or J 25 . If the GRAPHIC 8 system includes slave displays, the same dual coaxial/single coaxial cabling scheme may be used. The cables then connect the $R, G, B$ inputs on a slave display to the R,G,B loop-through connectors on a display monitor driven by the terminal controller.
3.5.3.2 Monochrome Monitor. Each monochrome display monitor in a GRAPHIC 8 system requires only one video input for proper operation: $\mathrm{B} / \mathrm{M}=\mathrm{blue} / \mathrm{monoch} r a n e$. Typically, a single coaxial cable connects the monitor input to connector J2 on the corresponding video controller card in the teminal controller. If a GRAPHIC 8 system includes a system interconnect panel, the video signal may be brought out via connector J16, J19, J22, or J25 on the panel.
3.5.4 INPUT DEVICE CONNECTIONS. The following paragraphs describe the cabling and connections required to interface keyboards, trackballs, forcesticks, data tablets, and PHOTOPENs wi th the GRAPHIC 8 terminal controller.
3.5.4.1 Reyboards. The model 5783 keyboard may be operated on any convenient flat surface within 50 cable feet ( 15.24 meters) of the terminal controller. The cable supplied with the keyboard is $72 \pm 2$ inches ( $183+5 \mathrm{~cm}$ ) long. The connector on the mating end of the keyboard cable is a type DAC-15P with hood. You need an adapter cable to accommodate this connector. The adapter cable (part number 1089765) connects between the keyboard cable and port 3 or port 7 (J4) on the multiport serial interface card in the terminal controller. If the GRAPHIC 8 system includes a system interconnect panel, the adapter cable (part number l088738) connects between the keyboard cable and connector J5 or J12 on the panel.

The model 5784 keyboard may be operated up to 100 feet (30.48 meters) from the terminal controller if a separate +5 V power supply (Acopian 5EB200 or equivalent) is provided.* The power supply mounts on the accessory panel (data entry device panel) of the associated display monitor. The power supply +15 V input comes from the terminal controller. Its +5 V output goes to the keyboard through the keyboard connector on the data entry device panel. As with the model 5783 keyboard, you need an adapter cable to connect between the cable supplied with the keyboard and either the multiport serial interface card or the system interconnect panel (if installed).

Table 3-4 lists the signals present at the pins of the keyboard cable connector.

[^0]Table 3-4. Keyboard Cable Connector, Pin Assignments

| PIN NUMBER | FUNCTION |
| :---: | :---: |
| 1 | Serial data from keyboard |
| 9 | Return |
| 11 | Serial data to keyboard |
| 4 | Return |
| 13,14 | +5V power |
| 10 | +15V power |
| 2 | -15V power |
| 6,7 | Power return |
| 8 | Chassis (safety) ground |
| 15 | LOCAL MODE signal (on 5783/5784 A and $B$ models only) |

3.5.4.2 Trackball and Forcestick. The model 5786 trackball and model 5787 forcestick may be operated on any convenient flat surface within 50 cable feet ( 15.24 meters) of the teminal controller. The cable supplied with a trackball or forcestick is $72 \pm 2$ inches ( $183 \pm 5 \mathrm{~cm}$ ) long. The connector on the mating end of the Erackball or forcestick cable is a type DA-15P. The trackball or forcestick cable connects to connector $J 4$ on the system interconnect panel. If a second trackball or forcestick is used, it should be connected to connector $J 13$ on the panel. The position entry device (PED) connected to J6 is called PEDI by the software and the PED connected to J13 is called PED2.

Table 3-5 lists the signals present at the pins of the trackball or forcestick cable connector.

Table 3-5. Trackball or Forcestick Cable Connector, Pin Assignments

| PIN NUMBER | FUNCTION |
| :---: | :---: |
| 1 | Signal out |
| 2 | -15 VDC |
| 3 | Not Used |
| 4 | Ground |
| 5 | Test Input |

Table 3-5. Trackball or Forcestick Cable Connector, Pin Assignments (Cont)

| PIN NUMBER | FUNCTION |
| :---: | :--- |
| 6 | Ground |
| 8 | Chassis ground |
| 9 | Ground |
| 10 | +15 VDC |
| 11 | Not Used |
| 12 | Not Used |
| 13 | +5 VDC |
| 14,15 | Not Used |

3.5.4.3 Data Tablet. The model 5788 data tablet may be operated on any convenient flat surface within 50 cable feet ( 15.24 meters) of the terminal controller. Like the model 5786 trackball and model 5787 forcestick, the data tablet is a position entry device (PED) and connects to either J6 or Jl3 on the system interconnect panel. An adapter cable connects between the $25-$ pin connector (JI) on the data tablet and the 15 -pin connector on the system interconnect panel.

Table 3-6 lists the signals present at the pins of the data tablet connector.

Table 3-6. Data Tablet Connector, Pin Assignments

| PIN NUMBER | FUNCTION |
| :--- | :--- |
| 1 | Not Used |
| 2 | RS232C Output |
| 3 | Clear to Send (A logic 0 stops data transmission) |
| 4 | Point/Run mode (logic $1=$ Run, logic $0=$ Point) |
| 5 | TTL Output |
| 6 | TTL Output |
| 7 | Signal Ground |
| 9 | Baud Rate Address Input $D$ |

Table 3-6. Data Tablet Connector, Pin Assignments (Cont)

3.5.4.4 PHOTOPEN. The model 5781 PHOTOPEN is supplied with a connector cable up to 74 inches (188 cm) long and either a five- or six-pin connector, depending on G-condition. The pHOTOPEN cable connects to connector J26 on the system interconnect panel, or directly to the PHOTOPEN number 1 (PP1) connector on the termimal controller. If a second PHOTOPEN is used, it should be connected to connector J27 on the system interconnect panel, or directly to the PHOTOPEN number 2 (PP2) connector on the terminal controller.

Table 3-7 lists the signals present at the pins of the PHOTOPEN cable connector.

Table 3-7. PHOTOPEN Cable Connector, Pin Assignments

| PIN NUMBER | FUNCTION |
| :---: | :--- |
| 1 (A) | +5 vac input |
| 2 (B) | Pulse output |
| 3 (C) | Not used |
| 4 (D) | Switch output |
| $5(E)$ | Ground |
| (F) | Not used |

NOTE: Pin designations in parentheses apply to the six-pin connector used on G-conditions $2,4,5$, and 7 . The five-pin connector is used on G-conditions 1,3 , and 6.
3.5.5 HARD COPY UNIT CONNECTIONS. The following paragraphs describe the cabling and connections required to interface monochrome and color hard copy units with the GRAPHIC 8 teminal controller.
3.5.5.1 Monochrome Hard Copy Unit. The power cable supplied with the monochrome hard copy unit varies, depending on the model number. Typically, for 120 V operation, the power cable comes with a 3-wire molded plug. For operation from other source voltages, the power cable comes without a connector plug. You must then add a suitable connector that fits your source of ac power. Detailed instructions for making power connections are contained in the hard copy unit technical manual. For proper operation, the hard copy unit must be connected to the same phase of primary power as the terminal controller.

The monochrome hard copy unit requires only one composite video input. The video controller card in the terminal controller produces six composite video outputs. Thus, any of the three ( $R, G, B$ ) video signals in a GRAPHIC 8 system may be used to drive the monochrome hard copy unit. However, for true gray scale rendition, an RGB mixer (part number 5809873) must be used. The RGB mixer accepts three color video signals from the video controller card and produces a single, composite video output. The output of the RGB mixer is then used to drive the hard copy unit.

Typically, a single coaxial cable connects $J 2$ on the video controller card to connector $J 701$ on the hard copy unit. If an RGB mixer is used, a dual coaxial cable connects J3 and J5 on the video controller card to the $R$ and $G$ inputs on the mixer. A single coaxial cable connects $J 7$ on the video controller card to the $B$ input on the mixer. If the GRAPHIC 8 system includes a system interconnect panel,
these signals may also be brought out via connectors J14 through J25 on the panel. A single coasial cable connects the output of the mixer to $J 701$ on the hard copy unit.

When remote control of the hard copy unit is required, control cable (part number 5810506 ) connects the appropriate port on the multiport serial interface card in the terminal controller to connector $J 790$ on the hard copy unit. (The multiport serial interface port must be reconfigured to represent port 5. Refer to Sanders manual H-81-0027 for information on the multiport serial interface. Refer to sanders manual H-80-0444 for hard copy unit programming instructions.) If the GRAPHIC 8 system includes a system interconnect panel, the control signals may be brought out via connector Jlo on the panel.
3.5.5.2 Color Hard Copy Unit. The power supplied with the color hard copy unit varies, depending on the source voltage. Typically, for 120 V operation, the power cable canes with a 3-wire molded plug. To avoid ground loop problems, the color hard copy unit should share a common ground with the teminal controller. For proper operation, the color hard copy unit must be connected to the same phase of primary power as the terminal controller.

The color hard copy unit requires three video inputs. Typically, a dual coaxial cable connects $J 3$ and $J 5$ on the video controller card to the RED and GREEN video input connectors on the color hard copy unit back panel. A single coaxial cable connects $J 7$ on the video controller card to the BLUE video input connector on the color hard copy unit back panel. If the GRAPFIC 8 system includes a system interconnect panel, the red and green video signals may be brought out via connectors J14 and J15, J17 and J18, J20 and J21, or J23 and J24 on the panel. Similarly, the blue video signal may be brought via connector J16, J19, J22, or J25.

### 3.6 SWITCH AND JUMPER SELECTIONS

Some of the circuit cards in the terminal controller contain jumper teminals and/or switches to allow selection of operating characteristics that differ from those normally preselected at the factory.

Determines the model number of your terminal controller (printed on the identification label attached to the bottom front panel of the teminal controller). Use figure 3-2 to determine the terminal controller configuration, number of displays per teminal controller, and the number of memory bits per pixel. You need this information to set the DIP switches on the circuit card assemblies. Figure 3-2 also relates the mapping memories and video controllers to their respective displays.


L identification label - see Chart below DIGITAL GRAPHIC CONTROLLER MAPPING MEMORY A FOR DISPLAY NO. 1 VIDEO CONTROLLER FOR DISPLAY NO. 1 MAPPING MEMORY B FOR DISPLAY NO. 1 MAPPING MEMORY A FOR DISPLAY NO. 2 VIDEO CONTROLLER FOR DISPLAY NO. 2 MAPPING MEMORY B FOR DISPLAY NO. 2 MAPPING MEMORY A FOR DISPLAY NO. 3 VIDEO CONTROLLER FOR DISPLAY NO. 3 MAPPING MEMORY B FOR DISPLAY NO. 3 TIMING MODULE ALWAYS IN AIT

REFERENCE CHART

| MODEL NO. | CONFIGURATION |
| :--- | :--- |
| $8215-N B$ | $512 \times 5121$ |
| $8216-N B$ | $512 \times 512 \mathrm{NI}$ |
| $8225-N B$ | $640 \times 4801$ |
| $8226-N B$ | $640 \times 480 \mathrm{NI}$ |
| $8235-N B$ | $1024 \times 7681$ |
| $8245-N B$ | $1024 \times 10241$ |
| $N=N U M B E R$ |  |

SEE ALSO FIGURE $1-6$.

EXAMPLE:
$8215-34=512 \times 512$, INTERLACED, 3 DISPLAYS, 4 BITS PER PIXEL
3.6.1 DISPLAY PROCESSOR II. The display processor II circuit card assembly contains two switches and one set of jumper points (see figure 3-3).


Figure 3-3. Display Processor II Switch and Jumper Locations
When E39 is connected to E40 (the standard configuration), the display processor II recognizes instruction 0 as a halt instruction. When E39 and E40 are open, instruction 0 is an illegal instruction.

Table $3-8$ defines the switch settings.

Table 3-8. Display Processor II Switch Settings

\begin{tabular}{|c|c|}
\hline SWITCH \& EUNCTION <br>
\hline \multirow[t]{8}{*}{Sl-1
S1-2
Sl-3

S1-4

S2-1
throug
S2-8} \& Not used <br>
\hline \& Open sets the number of address bits at 16 ; closed $=$ 18. Standard configuration is open. <br>
\hline \& Open sets the device number at 810 ; closed $=$ 1210. Standard configuration is open. <br>
\hline \& Open sets the cycle time for 400 ns operation; closed $=300 \mathrm{~ns}$. Standard configuration is open. <br>
\hline \& Contribute the last three digits of a 6-digit configuration register (the first three digits are understood to be zeros). Switches 7 and 8 make up the first digit (bit 8 is MSB): maximum value is 3. Switches 4-6 make up the second digit, and switches 1-3 make up the third digit. Switch closed = logic 1. Maximum value is 000377 . <br>

\hline \& | $$ |
| :--- |
| CONFIGURATION | <br>

\hline \&  <br>

\hline \& | $\begin{aligned} & C=\text { closed }=+ \\ & 0=\text { open } \end{aligned}$ |
| :--- |
| Register address 177774 is used to determine the number of keyboards defined in the system. | <br>

\hline
\end{tabular}

3.6.2 READ/WRITE MEMORY. For part number 1089724 (figure 3-4), confirm that switches are set and jumpers installed as shown in table 3-9.


Figure 3-4. Large Read/write Memory Switch Location

Table 3-9. Read/Write Memory Switches and Jumpers, Part No. 1089724

| MEMORY CONFIGURATION | SI SWITC CARD \#1 | SITIONS CARD \#2 | JUMPER CARD $\# 1$ | CTIONS CARD \#2 |
| :---: | :---: | :---: | :---: | :---: |
| 16K | $\begin{aligned} & \text { SI-1 thru } \\ & \text { SI-5 ON } \end{aligned}$ | N/A | E2-E3 | N/A |
| 32K | SI-1 ON | N/A | E2-E3 | N/A |
|  | $\begin{aligned} & \text { SI-2 thru } \\ & \text { SI-5 OFF* } \end{aligned}$ |  |  |  |
| 64 K | S1-1 ON | N/A | E2-E3 | N/A |
|  | $\begin{aligned} & \text { Sl-2 thru } \\ & \text { Sl-5 OFF* } \end{aligned}$ |  |  |  |
| 96 K | SI-1 ON | S1-1 OFF | E2-E3 | E2-E3 |
|  | $\begin{aligned} & \text { Sl-2 thru } \\ & \text { SI-5 OFF* } \end{aligned}$ | $\begin{aligned} & \text { Sl-2 thru } \\ & \text { Sl-5 OFF* } \end{aligned}$ |  |  |
| . 128K | SI-1 ON | S1-1 OFF | E2-E3 | E1-E2 |
|  | $\begin{aligned} & \text { Sl-2 thru } \\ & \text { Sl-5 OFF* } \end{aligned}$ | $\begin{aligned} & \text { Sl-2 thru } \\ & \text { Sl-5 OFF* } \end{aligned}$ |  |  |

*These switch positions allow access to all available memory. To mask out undesired memory blocks, set corresponding switch to ON.

For the G2 and G4 conditions of part number 1089724 , lift U16-2. Add jumper from U13-12 to U16-12.

For part number 5809944 (figure 3-5), confirm that jumpers are installed and that memory boundary switches Sl-1 through Sl-8 are set as shown in table $3-10$ for all memory configurations. Confirm that bank swap switches S2-1 through S2-4, page size switches S3-1 through S3-4, and mask area switches S4-1 through S4-8 are set as shown in table 3-11.

Table 3-10. Large Refresh Memory Switches and Jumpers, Part Number 5809944



Figure 3-5. Large Refresh Memory Switch and Jumper Locations

Table 3-11. Large Refresh Memory Bank Swap, Page Size, and Mask Area Switch Settings, Part No. 5809944

3.6.3 ROM AND STATUS CARD. This card (see figure 3-6) contains jumper terminals that allow reconfiguration of 15 different parameters, as described in table 3-12.

Table 3-12. ROM and Status Card Jumper Configurations

| FEATURE | JUMPER CONFIGURATION |
| :---: | :---: |
| Sync link interrupt level |  |
| Level 7 | E1 to E4 (normal configuration) |
| Level 6 | E2 to E4 |
| Level 5 | E3 to E4 |
| Display status interrupt level |  |
| Level 5 | E3 to E5 (normal configuration) |
| Level 6 | E2 to E5 |
| Level 7 | El to E5 |
| Transmit data select |  |
| TTY, RS-232C, or 20 mA current 100p | E7 to E8 (normal configuration) |
| TTL | E7 to E6 |
| Receive data termination |  |
| TTY | E35 to E36 (normal configuration) |
| TTL | E9 to E10; E35 to E36 open <br> E9 to Ell: E35 to E36 open |
| 20 mA current 100 p RS-232C | E9 to Ell; E35 to E36 open No jumper at E35, E36, E9 |
| Receive/transmit data |  |
| TTY | E12 to El3 open (normal configuration) |
| TTL or RS-232C | E12 to El3 |
| Word length select |  |
| 5 bits | E14 to E17 to El9 |
| 6 bits | El4 to E19 |
| 7 bits | E17 to E19 |
| 8 bits | El4 and El7 open (normal configuration) |

Table 3-12. ROM and Status Card Jumper Configurations (Cont)

| FEATURE | JUMPER CONFIGURATION |
| :---: | :---: |
| Receive/transmit parity |  |
| Checked and generated Disabled | E16 to E19 <br> El 6 open (normal configuration) |
| Parity select |  |
| Odd | E18 to E19 |
| Even | E18 open (nomal configuration) |
| Number of stop bits |  |
| 2 stop bits | E15 to El9 |
| I stop bit | El5 open (nomal configuration) |
| Receive/transmit frequency select |  |
| 110 baud | E20 to E22; E23 to E25 to E26 to E27 (nomal configuration) |
| 300 baud | E20 to E22; E24 to E26 to E27 |
| 1200 baud | E20 to E22; E26 to E27 |
| 2400 baud | E20 to E22; E23 to E25 to E27 |
| 4800 baud | E 20 to E22; E23 to E24 to E27 |
| 9600 baud | E20 to E22; E23 to E27 |
| 50 K baud | E20 to E21 |
| System clock mode |  |
| Internal clock | E28 to E29 (normal configuration) |
| External clock | E29 open |
| Address selection | * |
| 16 K to 24 K | E44 to E32 |
| 20 K to 28 K | E30 to E32 |
| 24K to 32K | E31 to E32 |
| Status logic enable |  |
| Enabled Disabled | E33 to E34 (nomal configuration) E33 open |

Table 3-12. ROM and Status Card Jumper Configurations (Cont)

| geature | JUMPER CONEIGURATION |
| :---: | :---: |
| ```Trap address memory locations 0-8k 8K - 16K 16K-24K 24K-32K 50R/800R baud select 50 kHz 800 kHz``` | E37, E38, E39, E40 open (normal configuration) <br> E39 to E40 <br> E37 to E38 <br> E37 to E38; E39 to E40 <br> E42 to E43 (normal configuration) E41 to E43 |



Figure 3-6. ROM and Status Card Jumper Locations
3.6.4 MULTIPORT SERIAL INTERFACE. This card (figure 3-7) contains both jumper terminals and DIP switches that allow reconfiguration of 14 different parameters, as described in table 3-13.


Figure 3-7. Multiport Serial Interface Switch and Jumper Locations
Figure $3-8$ shows the normal switch positions for the standard port configuration. The + mark on the switch indicates the ON side. The dots in the figure indicate the side of the switch that is pushed down. Table $3-12$ A shows other possible arrangements.


419


439


490

Figure 3-8. Multiport Serial Interface, Normal Switch Positions

When setting up these assignments, do not use the same switch settings for more than one port; only the lowest numbered port will respond to the device. If two serial interface cards are used, ports 5 through. 8 are programmed in the same manner as ports 1 through 4. Do not duplicate any card $\# 1$ switch settings on card \#2; if there is a duplication, neither port will work.

Table 3-12A. Serial Interface Port Device Assignments

| DEVICE | $\begin{aligned} & \text { PORT } \\ & \text { SI } \end{aligned}$ | $\mathrm{l}_{\mathrm{S} 2}$ | 19) | PORT S4 | ${ }_{5}^{2}$ | 19) S6 | $\begin{aligned} & \text { PORT } \\ & \text { SI } \end{aligned}$ | 3 $S 2$ | 39) | $\begin{gathered} \text { PORT } \\ \text { S4 } \end{gathered}$ | $\begin{aligned} & 4( \\ & 55 \end{aligned}$ | $\begin{gathered} 39) \\ 56 \end{gathered}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Communicator (RS-232-C) | On | On | On | - | - | - | - | - | - | - | - | - |
| Reyboard \#1 | Off | Off | On | On | Off | On | On | Off | On | On | off | On |
| PED \#1 | On | Off | Off | On | Off | Off | On | Off | Off | On | Off | Off |
| Hardcopy Unit | Off | On | On | Off | On | On | Off | On | On | Off | On | On |
| Reyboard \#2 | Off | Off | On | Off | Off | On | Off | Off | On | Off | Off | On |
| PED \#2 | Off | Off | off | Off | Off | off | Off | Off | Off | Off | Off | Off |
| Spare \#1 | On | On | off | On | On | Off | On | On | Off | On | On | Off |
| Spare \#2 | Off | On | Off | Off | On | off | Off | On | Off | Off | On | Off |

Underlined switch settings are the normal configuration. Unless otherwise specified by the customer, switches are set to these positions at the factory.

Table 3-13. Multiport Serial Interface Parameter Selections


Table 3-13. Multiport Serial Interface Parameter Selections (Cont)


Table 3-13. Multiport Serial Interface Parameter Selections (Cont)


Table 3-13. Multiport Serial Interface Parameter Selections (Cont)

| FEATURE | CONEIGURATION |
| :---: | :---: |
| Parity (port 1 only) <br> Parity disabled <br> Parity enabled <br> Oda parity <br> Even parity <br> Data terminal ready (port 1 only) <br> Circuit enabled <br> Circuit disabled <br> Interrupt level select <br> Level 5 <br> Level 6 (normal configuration) <br> Level 7 |  |
|  | U80-S1 off |
|  | U80-S1 on |
|  | U80-S2 on |
|  | U80-S2 Off |
|  | NOTE |
|  | ty is disabled, there are ata bits. If parity is enthere are seven data bits. |
|  |  |
|  | U39-S7 on |
|  | U39-S7 off |
|  |  |
|  | E1 to E3 |
|  | E2 to E3 |
|  | E4 to E3 |

3.6.5 PARALLEL INTERFACE. The model 5712 parallel interface card contains 105 teminals that allow you to customize the card for special installations. Most of the terminals (El through E43 and E49 through E66) plus a 2.35 by 6.5 inch ( 6 by 16.5 cm ) unused area are for use with additional integrated circuit elements, which can interface with devices that have unique signal definitions, signal polarities, handshaking requirements, and/or driver/receiver and line matching requirements.

The remaining teminals let you change certain operating parameters, as described in table 3-14.

Table 3-14. Parallel Interface Parameter Selections

| FEATURE | CONF IGURATION |
| :---: | :---: |
| Register address select |  |

Register address select
First card, address 17241X (normal configuration)

Second card, address 17243X
Third card, address 17245X
Fourth card, address 172478
Intersupt trap address select First card (normal configuration) Second card

Third card

Fourth card

Word vs byte mode
Word mode (nomal configuration)
Byte mode
Polarity of INIT signal from host Low (INIT-) produces SYST-B High (INIT+) produces SYST-B SYST-B inhibited

Interrupt priority level
Level 6 (nomal configuration)
Level 5
Level 7

E96 to E99

E98 to E99
E97 to E99
E95 to E99

E87, E88 open; E47, E45 open
E87 to E88, E46 to E47; E45 open

E87 to E88, E44 to E45; E47 open

E87 to E88, E44 to E45, E46 to E47

E90 open
E89 to E90

E93 to E94
E92 to E94
E91 to E94

E83 to E85
E84 to E85
E86 to E85

Table 3-14. Parallel Interface Parameter Selections (Cont)

| FEATURE | CONFIGURATION |
| :---: | :---: |
| Clock speed |  |
| CLOK-F (10 MHz) (nomal configuration) | E100 to El01 |
| Clock rate divided (for long distance interface) | Open El0 to ElOI. Connect El00 to input of a suitable divider. Connect divider output to El01. (Divider may be added to the parallel interface card or located externally with connections made through unused backplane connections). |
| Output data enable ODEN- signal fran host |  |
| ODEN- enabled | E102 to E103 |
| ODEN- disabled | E102, El03 open |
| Input data enable function |  |
| Ground enable to input drivers | E104 to El05 |
| Ground enable not required | E104, El05 open |
| Additional integrated circuit connections |  |
| NOTE |  |
| These connections allow additional integrated circuits to be added to the card. |  |
| Connect attention interrupt enable to IC | E48 |
| Accept interrupt requests from IC | E67 |
| Connect resets to IC | E68 |

Table 3-14. Parallel Interface Parameter Selections (Cont)

| FEATURE | CONF IGURATION |
| :---: | :---: |
| Additional integrated circuit connections (Cont) <br> Connect fixed logic high to IC <br> Connect +5 V to IC <br> Connect -15 V to IC <br> Connect ARET- to IC <br> Connect +15 V to IC <br> Accept reset from IC <br> Card device select <br> Normal configuration is: <br> DEVO-B high <br> DEV1-B low <br> DEV2-B high <br> DEV3-B active <br> Device codes for second, thir created by connecting Ell2 to | E69 <br> E70 through E78 <br> E79 <br> E80 <br> E81 <br> E82 <br> E112, E113 open <br> E114 to E115 <br> E116, E117 open <br> E110 to Ell1 <br> nd fourth cards can be 3. Ell6 to El17, or both. |

3.6.6 DIGITAL GRAPHIC CONTROLLER. This card contains two DIP switches to accamodate three types of PROM devices and two screen configurations. See figure 3-9.

> SET SWITCHES FOR MODEL NUMBER (TABLE C)



| TABLEA |  |  |  |
| :---: | :---: | :---: | :---: |
| Switer | DEVICE TYPE |  |  |
|  | 03636 | MK2716 | 2732 |
| 52-1 | 0 | 0 | 0 |
| S2-2 | 0 | $c$ | $c$ |
| 52-3 | 0 | 0 | $c$ |
| S2-4 | 0 | 0 | 0 |
| 52-5 | $c$ | $\bigcirc$ | 0 |
| S2-6 | $c$ | $\bigcirc$ | 0 |
| 52-7 | 0 | c | $c$ |
| S2-8 | $c$ | 0 | 0 |
| \$1-6 | 0 | 0 | 0 |
| 51-8 | $E$ | $C$ | C |

$C=$ CLOSED
$O=$ OPEN

| TAGLE B |  |  |
| :---: | :---: | :---: | :---: |
| DEVICE <br> TYPE $51-1$ $51-2$ $51-3$ <br> D363s $C$ 0 0 <br> MK2716, $C$ 0 $c$ <br> 2732    |  |  |

TABLE C

| MOOEL <br> NUMBER | $51-4$ | $51-5$ | $51-7$ |
| :--- | :---: | :---: | :---: |
| 8215,8216, | 0 | $C$ | $C$ |
| 8225,8226 |  |  |  |
| 8235,8245 | 0 | $C$ | 0 |

3.6.7 VIDEO CONTROLLER. This card contains three DIP switches to select the video blink, display number, and shift register configuration. See figure $3-10$.


Figure 3-10. Video Controller Switches
3.6.8 MAPPING MEMORY, There are many configurations of mapping memory (see table 3-15). They are all similar in layout; the differences are in the components used. All the mapping memories listed in table $3-15$ are altered items, made from one of the following basic boards:
5802554 - basic 64 R board
5809875 - high speed 64 K board
5977176 - basic 16 K board

Table 3-15. Mapping Memory Configurations

| PART NO. | TYPE | RESOLUTION X BITS/PIXEL/BLINK | $\begin{aligned} & \text { INPUT } \\ & \text { BITS } \end{aligned}$ | $\begin{aligned} & \text { PIXEL } \\ & \text { BITS } \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: |
| 5802966 | 64K | $1024 \times 1024 \times 4$, blink | 0-3 | $0-2,7$ |
| 5802967 | 64R | $1024 \times 1024 \times 4$ | 0-3 | 0-3 |
| 5802968 | 64R | $1024 \times 1024 \times 4$, blink | 4-7 | 4-7 |
| 5809855 | 16K | $512 \times 512 \times 4$ (2 displays), blink | 0-3 | $0-2,7$ |
| 5809970 | 64K | ```1024 x 1024 8 4 (high speed), blink``` | 0-3 | $0-2,7$ |
| 5809971 | 64K | ```1024 x 1024 * 4 (high speed), blink``` | 4-7 | 4-7 |
| 5809972 | 64K | $1024 \times 1024 \times 4$ (high speed) | 0-3 | 0-3 |
| 5809973 | 64K | $512 \times 512 \times 8$ (high speed), bilink | 0-7 | 0-7 |
| 5810013 | 64K | $1024 \times 512 \times 4$ (dual, high speed) blink | 0-3 | $0-2,7$ |
| 5977268 | 16K | $512 \times 512 \times 4$, blink | 0-3 | 0-2, 7 |
| 5977269 | 16K | $512 \times 512 \times 4$ | 0-3 | 0-3 |
| 5977270 | 16 K | $512 \times 512 \times 4$, blink | 4-7 | 4-7 |
| 5977271 | 16 K | $1024 \times 512 \times 2$ | 0-1 | 0-1 |
| 5977272 | 16K | $1024 \times 512 \times 2$ | 2-3 | 2-3 |
| 5977273 | 16K | $1024 \times 512 \times 2$ | 4-5 | 4-5 |
| 5977274 | 16R | $1024 \times 512 \times 2$, blink | 6-7 | 6-7 |
| 5977275 | 16K | $1024 \times 512 \times 2$, blink | 2-3 | 2-7 |
| 5977276 | 64 R | $1024 \times 512 \times 4$, blink | 0-3 | 0-2, 7 |
| 5977277 | 64R | $1024 \times 512 \times 4$ | 0-3 | 0-3 |

3-40 Change 1

Table 3-15. Mapping Memory Configurations (Cont)

| PART NO. | TYPE | RESOLUTION X BITS/PIXEL/BLINK | $\begin{aligned} & \text { INPUT } \\ & \text { BITS } \end{aligned}$ | PIXEL BITS |
| :---: | :---: | :---: | :---: | :---: |
| 5977278 | 64K | $1024 \times 512 \times 4, b l i n k$ | 4-7 | 4-7 |
| 5977279 | 64K | $1024 \times 512$ (or 5122) $\times 8$, blink | 0-7 | 0-7 |
| 5977280 | 16R | $1024 \times 1024 \times 1$ | 0 | 0 |
| 5977281 | 16R | $1024 \times 1024 \times 1$ | 1 | 1 |
| 5977282 | 16K | $1024 \times 1024 \times 1$ | 2 | 2 |
| 5977283 | 16R | $1024 \times 1024 \times 1$ | 3 | 3 |
| 5977284 | 16K | $1024 \times 1024 \times 1$ | 4 | 4 |
| 5977285 | 16K | $1024 \times 1024 \times 1$ | 5 | 5 |
| 5977286 | 16K | 1024 $\times 1024 \times 1$ | 6 | 6 |
| 5977287 | 16 R | $1024 \times 1024 \times 1$. blink | 7 | 7 |
| 5977288 | 16R | $1024 \times 1024 \times 1$, blink | 3 | 7 |
| 5977289 | 64K | $1024 \times 1024 \times 2$ | 0-1 | 0-1 |
| 5977290 | 64K | $1024 \times 1024 \times 2$ | 2-3 | 2-3 |
| 5977291 | 64 K | $1024 \times 1024 \times 2$ | 4-5 | 4-5 |
| 5977292 | 64 K | $1024 \times 1024 \times 2$, blink | 6-7 | 6-7 |
| 5977293 | 64 R | $1024 \times 1024 \times 2$, blink | 2-3 | 2-7 |
| 5977305 | 64 K | $1024 \times 512 \times 4$ (dual), blink | 0-3 | 0-2, 7 |

See figure 3-11 for mapping memory switch settings.

4.82.0178.033
table A

| MOOEL NO. | OPTION |
| :---: | :---: |
| $82.15 \mathrm{~m} \times 4$ | 3. |
| 8215-x | 4 |
| $2216-\times 4$ | 3 |
| $82160 \times 8$ | 4 |
| 8225-x4 | 1 |
| 2225-x8 | 2 |
| $82250 \times 4$ | 1 |
| $5260 \times 8$ | 1 |
| $8235 \times 4$ | 1 |
| 玉235-x9 | 1 |
| 8245-X4 | 1 |
| 8245-X8 | 1 |


| SWITCH | DISPLAYNO. |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  | 1 | 2 | 3 | 4 |
| U174el | 0 | 0 | 0 | 6 |
| U114-2 | 0 | 0 | 6 | 0 |
| U1143 | 0 | 6 | 0 | 0 |
| U114-4 | c | 0 | 0 | $\bigcirc$ |
| U17403 | c | $\bigcirc$ | c | C |
| U1146 | 0 | 0 | 0 | 0 |
| U11-7 | 0 | 0 | 0 | 0 |
| U114-8 | 0 | 0 | 0 | $\bigcirc$ |
| U152-1 | 0 | 0 | 0. | $c$ |
| U132-2 | 0 | $c$ | 0 | $\bigcirc$ |
| U132-3 | 0 | 0 | C | 0 |
| U123-4 | $C$ | 0 | 0 | 0 |


| SWITCH | DISPLAYNO. |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  | 1 | 2 | 3 | 4 |
| U114-1 | 0 | 0 | 0 | 6 |
| U114-2 | 0 | 0 | c | $\bigcirc$ |
| U114.3 | $\bigcirc$ | $c$ | 0 | 0 |
| U114-4 | $c$ | 0 | 0 | 0 |
| U114-5 | $c$ | $c$ | $c$ | 6 |
| U11406 | 0 | 0 | 0 | 0 |
| U114-7 | 0 | 0 | 0 | 0 |
| U114-8 | 0 | 0 | 0 | 0 |
| U132-1 | 0 | $\bigcirc$ | 0 | 0 |
| U138-2 | 0 | 0 | 0 | 0 |
| U132-3 | 0 | 0 | 0 | 0 |
| Uyse-4 | 0 | 0 | 0 | 0 |

PAgLE S

| SWITCH | OPIION |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  | 1 | 2 | 3 | 4 |
| U123-1 <br> U123-2 <br> U123-3 <br> U123-a <br> U123-5 <br> U123-6 <br> U123-7 <br> U123-3 | 0 0 6 0 0 6 0 0 | 0 0 6 0 0 0 0 0 | $C$ 0 0 6 0 0 0 0 | 0 0 0 0 0 0 0 0 |
| U133-1 <br> U133-2 <br> 4133-3 <br> U133-4 <br> U133-5 <br> U133-6 <br> U133-7 <br> U133-8 | 0 0 0 0 0 0 0 0 | 0 $C$ 0 0 0 0 0 0 | 1 0 0 0 0 0 6 0 | 0 6 0 0 0 0 0 0 |
| 4134-1 <br> U134-2 <br> U134-3 <br> U134-4 <br> U134-5 <br> U134 6 <br> U134. 7 <br> UI34-8 | 0 6 6 0 0 6 0 0 | 0 $C$ 0 6 0 6 0 0 | 0 6 0 6 0 0 0 0 | 0 0 6 0 0 0 0 0 |

TAate 0

| MEMORY | U132 |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| SELECT | 5 | 6 | 7 | 3 |
| $A$ | $C$ | 0 | 0 | $C$ |
| 8 | 0 | $C$ | $C$ | 0 |

TABLE E

| SW | U123 | U133 | U134 |
| :---: | :---: | :---: | :---: |
| 1 | 0 | 0 | 0 |
| 2 | 0 | 6 | 6 |
| 3 | 6 | 0 | 0 |
| 4 | 0 | 0 | 6 |
| 5 | 0 | 0 | 0 |
| 6 | 6 | 0 | 6 |
| 7 | 0 | 0 | 6 |
| 8 | 0 | 0 | 0 |

TABLE G

| 4132 |  |  |  |
| :---: | :---: | :---: | :---: |
| 5 | 6 | 7 | 8 |
| 0 | 0 | 0 | 0 |

$0=$ OPEN
$C=$ CLOSED

Figure 3-11. Mapping Memory Switches (Sheet 2 of 2)
3.6.9 TIMING MODULE. This card contains five DIP switches to select the number of bits per pixel, the number of bits per memory board, and the number of displays per terminal. See figure $3-12$.


| SWHTCH | MODEL NUMBER |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 8215 | 8216 | 8225 | 8226 | 8235 | 8245 |
| $\begin{aligned} & U-1 \\ & U 6-4 \\ & U S-5 \\ & U-6 \end{aligned}$ | $\begin{aligned} & 0 \\ & 0 \\ & 0 \\ & 0 \end{aligned}$ | $\begin{aligned} & 0 \\ & 0 \\ & 0 \\ & 0 \end{aligned}$ | $\begin{aligned} & 0 \\ & 6 \\ & 0 \\ & 0 \end{aligned}$ | $\begin{aligned} & 0 \\ & e \\ & 0 \\ & 0 \end{aligned}$ | 0 6 0 0 | $\begin{aligned} & 0 \\ & 6 \\ & 6 \\ & 0 \end{aligned}$ |
| Ullol <br> U10-8 <br> 410 m <br> 410 m 4 <br> U10-5 <br> 110-6 <br> U10-7 <br> U10-8 | $\begin{aligned} & 6 \\ & 6 \\ & C \\ & 6 \\ & 0 \\ & 0 \\ & 6 \\ & 0 \end{aligned}$ | SEE <br> NOTE <br> 1 | $\begin{aligned} & 0 \\ & 6 \\ & 6 \\ & 6 \\ & 0 \\ & 0 \\ & 6 \\ & 0 \end{aligned}$ | SE: NOTE 1 | $\begin{aligned} & c \\ & 0 \\ & c \\ & c \\ & 0 \\ & 0 \\ & c \\ & 0 \end{aligned}$ | 0 0 6 6 0 0 0 0 |
| $\begin{aligned} & \text { U12-8 } \\ & \text { U12-2 } \\ & \text { U12-3 } \\ & \text { U12-4 } \\ & \text { U12-S } \\ & \text { U12-b } \end{aligned}$ |  | $\begin{aligned} & c \\ & c \\ & 0 \\ & 0 \\ & c \\ & c \end{aligned}$ | $\begin{aligned} & c \\ & c \\ & 0 \\ & 0 \\ & 0 \\ & 0 \end{aligned}$ | $\begin{aligned} & c \\ & c \\ & 0 \\ & 0 \\ & 0 \\ & c \end{aligned}$ | $\begin{aligned} & C \\ & c \\ & 0 \\ & 0 \\ & c \\ & 0 \end{aligned}$ | $C$ $C$ 0 0 0 0 |
| U30-1 <br> 430-2 <br> 430 <br> $1000-4$ <br> 1000-3 <br> 1000 <br> 43007 <br> 430 m | $\begin{aligned} & 0 \\ & c \\ & 6 \\ & 0 \\ & 0 \\ & e \\ & 0 \\ & 0 \end{aligned}$ | $\begin{aligned} & 0 \\ & c \\ & 6 \\ & 0 \\ & 0 \\ & 6 \\ & 0 \\ & 0 \end{aligned}$ | 0 6 6 0 0 6 0 0 | 0 6 0 0 0 6 0 0 | $\begin{aligned} & 0 \\ & c \\ & e \\ & 0 \\ & 0 \\ & C \\ & C \\ & 0 \end{aligned}$ | $\begin{aligned} & 0 \\ & c \\ & c \\ & 0 \\ & 0 \\ & c \\ & c \\ & 0 \end{aligned}$ |
| $\begin{aligned} & 437-1 \\ & 477-2 \\ & 437-3 \\ & 437-4 \\ & 437-5 \\ & 4376 \\ & 137-7 \\ & 437-8 \end{aligned}$ | 6 0 0 0 0 6 0 0 | sex NOTE 1 | 0 6 0 0 0 $e$ 0 0 | SEE NOTE 1 | $\begin{aligned} & 0 \\ & 0 \\ & 0 \\ & 6 \\ & 0 \\ & 0 \\ & 0 \\ & 0 \\ & \hline \end{aligned}$ | 0 0 0 6 0 0 0 0 |

NOTE 1: DETERMINE OPTION NUMBER FROM TABLE B, THEN SET UIO AND LB7 PER TABLE C.

TABEG

|  | NUMBER OF DISPLAYS |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| SWITCH | 1 | 2 | 3 | 4 |
| $U-7$ | 6 | 0 | 6 | 0 |
| $U S-8$ | 6 | 6 | 0 | 0 |

$0=$ OPEN $C=$ ClOSED

TABLE 8

| $\begin{aligned} & \text { PART } \\ & \text { NUABER } \end{aligned}$ | $\begin{array}{cc} \hline \text { MODEL NUMBER } \\ 8216 & 8226 \\ \hline \end{array}$ |  |
| :---: | :---: | :---: |
| 597716861 5977168G2 5805995 G 1 | OFPION 1 OPTION 2 OPTION 2 | OPTION 3 OPTION 4 OPTION 4 |

TABLE $G$

|  | OPTION |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| switer | 1 | 2 | 3 | 4 |
| U10-1 | $c$ | C | C | $\bigcirc$ |
| 410-2 | 0 | $c$ | 0 | $c$ |
| U10-3 | $c$ | 0 | 0 | $c$ |
| 41004 | 0 | $c$ | $c$ | $c$ |
| U10-5 | $c$ | $c$ | $c$ | $c$ |
| 410\% | 0 | $c$ | 0 | 6 |
| U10.7 | c | 0 | $c$ | 0 |
| 410-8 | 0 | 0 | 0 | 0 |
| . 1377 | $c$ | 0 | 0 | 0 |
| 137-2 | 0 | $c$ | c | $\bigcirc$ |
| 187-3 | 0 | 0 | - | 0 |
| 137-4 | $c$ | 0 | $c$ | $c$ |
| 137-5 | 0 | c | 0 | 0 |
| L3780 | 0 | 0 | 0 | 0 |
| -137-7 | 0 | 0 | 0 | $\bigcirc$ |
| 137-8 | $c$ | 0 | C | $\bigcirc$ |

TABLED

|  | BITS/PIXEL |  |  |
| :---: | :---: | :---: | :---: |
| SWITCH | 2 | 4 | 8 |
| $U-2$ | 0 | $C$ | 0 |
| $U 6-3$ | $C$ | 0 | 0 |

TABLEE


TABLE F

|  | $8 P B$ |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| SWITCH | 1 | 2 | 4 | 8 |
| $U 12-7$ | $C$ | 0 | $C$ | 0 |
| $U 12-8$ | $C$ | $C$ | 0 | 0 |

3.6.10 TIMING MODULE II. This card contains 8 DIP switches to select the number of bits per pixel, the number of bits per memory board, and the number of displays per terminal. See figure 3-13.


Figure 3-13. Timing Module II Switches (Sheet 1 of 2)

3-46 Change 3
table A

| SWITCH | Miodel number |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 8215 | 8216 | 8205 | 8226 | 8235 | 8245 | 8256 |
| US-2 <br> USM <br> USㅇ <br> US.7 | $\begin{aligned} & 0 \\ & 0 \\ & 0 \\ & 0 \end{aligned}$ |  | 0 0 6 0 | $\begin{aligned} & 0 \\ & 0 \\ & 0 \\ & 0 \end{aligned}$ | $c$ 0 $e$ 0 | $c$ 0 $e$ 0 | $c$ $c$ $e$ 0 |
| U17-1 <br> U17-2 <br> U17-3 <br> U17-4 <br> U17-7 <br> U17-6 | 0 0 0 0 0 0 | 0 0 0 0 6 6 | 0 0 0 0 0 0 | 0 0 0 0 0 0 | 0 0 0 0 0 0 | 0 0 0 0 0 0 | 0 0 0 0 0 0 |
| प27 m U27-2 <br> U27-3 <br> U27-4 <br> U27-5 <br> U27-6 <br> UTV-8 |  | $e$ $c$ 0 6 6 6 0 0 | 0 $c$ 6 6 0 6 6 0 | 0 $c$ 0 6 0 6 6 0 |  | $\begin{aligned} & 0 \\ & 0 \\ & c \\ & c \\ & 0 \\ & 0 \\ & e \\ & 0 \end{aligned}$ | 0 0 0 6 6 0 6 0 |
| U44: U402 4 U424 U44-5 U446 U44-7 | 6 0 0 0 0 0 0 0 | $C$ 0 0 0 0 0 0 0 | 6 0 0 0 0 0 0 0 | $\begin{aligned} & 0 \\ & c \\ & 0 \\ & 0 \\ & 0 \\ & 0 . \\ & 0 \\ & 0 \end{aligned}$ | 6 0 0 0 6 0 0 0 | 6 0 0 0 0 0 0 0 | $\begin{aligned} & c \\ & 0 \\ & 0 \\ & 0 \\ & e \\ & 0 \\ & 0 \\ & 0 \end{aligned}$ |
|  | 0 0 0 0 0 0 0 | 0 0 0 0 0 0 0 0 | 0 0 0 0 0 0 0 0 | 0 0 0 0 0 0 0 0 | $\begin{aligned} & 0 \\ & 0 \\ & 0 \\ & 0 \\ & 0 \\ & 0 \\ & 0 \\ & 0 \end{aligned}$ | $\begin{aligned} & 0 \\ & 0 \\ & 0 \\ & 0 \\ & 0 \\ & 0 \\ & 0 \\ & 0 \end{aligned}$ | 0 0 0 0 0 0 0 0 |
| $\begin{aligned} & U 59-1 \\ & U 59-2 \\ & U 59-3 \\ & U 59-4 \end{aligned}$ | $c$ 0 $c$ 0 | $c$ 0 $e$ 0 | $c$ 0 $C$ 0 | $c$ 0 $c$ 0 | $\begin{aligned} & c \\ & 0 \\ & c \\ & 0 \end{aligned}$ | $c$ 0 $e$ 0 | 0 0 0 0 |

TABLE B

|  | BITS PER PIXEL |  |  |
| :--- | :--- | :--- | :--- |
| SWTCH | 2 | 4 | 8 |
| $U 5-3$ | $C$ | 0 | 0 |
| $15-5$ | 0 | $C$ | 0 |

table c

|  | BITS PER MAPPING MEMORY |  |  |  |
| :--- | :--- | :--- | :--- | :--- |
| SWITCH | 1 | 2 | 4 | 8 |
| U17-5 | $C$ | $C$ | 0 | 0 |
| U17-S | $C$ | 0 | 6 | 0 |

TABLE D

|  | Number OF DISPLAYS |  |  |  |
| :--- | :--- | :--- | :--- | :--- |
| SWITCH | 1 | 2 | 3 | 4 |
| US-1 | $C$ | 0 | 6 | 0 |
| $U S-8$ | $C$ | $C$ | 0 | 0 |

table $E$
OUTPUT SYNC LOGIC SELECT

| $\begin{gathered} C \text { SINC } \\ 15 \\ 440-3 \end{gathered}$ |  | $\begin{aligned} & \text { H SYNC } \\ & 16 \\ & 40-1 \end{aligned}$ |  | $\begin{aligned} & V \text { SYNC } \\ & J 7 \\ & \text { U40-2 } \end{aligned}$ |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| POS | NEG | POS | NEG | POS | NEG |
| $c$ | $\bigcirc$ | $c$ | $\bigcirc$ | $c$ | 0 |

Figure 3-13. Timing Module II Switches (Sheet 2 of 2 )


### 4.1 GENERAL

This section contains the maintenance philosophy, test equipment required, troubleshooting instructions, adjustments, and repair information.

### 4.2 MAINTENANCE PHILOSOPHY

The maintenance philosophy for a typical GRAPHIC 8 system is to isolate faults to the equipment unit or system camponent causing the fault. You can then make the appropriate adjustment, alignment or repair by following the instructions given in the applicable technical manual for that unit. In particular, the maintenance philosophy for the teminal controller is to limit repairs to replacement of plug-in circuit cards and chassis-mounted components. This approach reduces system down-time if a failure occurs. Field repair of circuit cards is not recammended because of their complexity. Testing them requires special factory-level test equipment.

If you encounter a failure, try to isolate the failure to a specific circuit card, then replace that card and make any required adjustments. If the system resumes nomal operation, the card that you took out is defective. Return that card to Sanders for repair. Send us also a description of the symptoms that led you to the failed card.

### 4.3 TEST EQUIPMENT REQUIRED

The following equipment (or equivalent) is recommended for maintenance of the terminal controller:

Oscilloscope
Digital voltmeter
Card extender

Tektronix type 465 with lox probe Eluke model 8000A Sanders part no. 4171110

### 4.4 TROUBLESHOOTING INSTRUCTIONS

Troubleshooting the GRAPHIC 8 system involves checking the results of the terminal controller built-in diagnostics, observing the teminal controller verification test pattern, and following the steps shown on the troubleshooting flow diagram. The following paragraphs give the instructions needed to use these techniques, along with some additional hints that may be used when isolating faults to individual plug-in circuit cards.
4.4.1 BUILT-IN DIAGNOSTICS. When the terminal controller is initialized in the SYSTEM mode, it executes a set of diagnostic routines that test the GO/NO GO status of certain circuit cards. If a NO GO is detected, the terminal controller sets a bit in the diagnostic error word, and an error message containing the error word is sent to the host computer. The message indicates the diagnostic routine that failed, but does not specifically state how the routine failed.

When you initialize the teminal controller in the LOCAL mode, it executes the same set of diagnostic routines, then executes the teminal verification pattern. The results of the diagnostic routines are displayed in the left field of the readout box as an octal number with the letter $T$ above it (the letter $T$ means that the 3 -digit code is the result of the diagnostic routines).

If all tests pass, the 3 -digit number is 000 , assuming all cards are installed. If any card is not installed, the bit for that card is set, producing an error indication.

If more than one test fails, and sequencing through the remaining tests does not stop, the readout is the sum of all the failed tests. For example, if the teminal controller does not contain a 3D coordinate converter, and both the memory and the graphic controller diagnostics fail, the readout is 045.

The following paragraphs describe the diagnostic routines.
Serial Interface Diagnostic. This test verifies initialization of the status registers.

Parallel Interface Diagnostic. This test verifies initialization of the handshaking circuits and status registers.

Display processor Diagnostic. This test verifies execution of a branch instruction, an interrupt sequence, and single-word, doubleword, and triple-word instructions from the instruction set.

Memory Diagnostic. This test verifies the ability to write and read at each memory location. (During this test, any data already contained in a memory location is temporarily stored in a general purpose register, then restored to its location.)

Graphic Controller Diagnostic. This test verifies that:

1. Each display register is initialized.
2. The graphic controller is in its proper state.
3. The display processor can start a refresh sequence.
4. The graphic controller can start, run, access memory, perform refresh functions, and stop.

3D Coordinate Converter Diagnostic. This test verifies the 3D coordinate converter's ability to write and execute a simple instruction.
4.4.2 VERIFICATION TEST PATTERN. The teminal controller verification test pattern can be used with up to two display indicators and a full complement of peripherals. The pattern verifies proper operation of the terminal controller and display indicators. Each area of the verification test pattern has a specific function: either executing a system parameter or providing a visual indication for fault isolation. Figure $4-1$ shows the nomal indications expected when using the verification test pattern; figure 4-2 shows some common error indications.
4.4.2.1 Character Verification. The character area of the test pattern displays the character repertoire, including alphanumerics and special symbols. In addition, the letters EM are displayed in three different sizes.
4.4.2.2 PED Manipulation. A set of numerals and asterisks should appear at the upper left corner of the screen. The numerals indicate the PED assigned to the symbol. There may be up to eight symbols present, depending on the input device configuration as set on the display processor.
4.4.2.3 Polygon Fill. The triangle directly below the monitor number will cycle through all available colors in the particular configuration.
4.4.2.4 Point Plot. All point plot instructions are used to build the geometric plot-in the center of the screen.
4.4.2.5 Overflow Indicators. Various full-screen vectors will appear in the test pattern if $X$ or $Y$ overflow intercupts occur unexpectedly or if they fail to occur when they are expected. See figures 4-1 and 4-2.
4.4.3 USING THE TROUBLESHOOTING FLOW DIAGRAM. FOllow the steps indicated in figure 4-3 to isolate a trouble to a circuit card or equipment unit. pay particular attention to the cautions of sheet 1 of the figure. Figure 4-3 applies specifically to the standard card cage and circuit card complement; however, the same general procedure applies when troubleshooting the extended card cage. The slot numbers of the individual circuit cards differ from the standard card cage to the extended card cage; refer to figure 1-7 for extended card cage card locations.
4.4.4 CARD CAGE CIRCUIT CARD INSTALLATION. The circuit cards that connect to the processor bus (cards in slots XAl through XA7 in the standard card cage, cards in slots XA4 through XAll in the extended card cage) can be installed in other arrangements than that shown in figure 4-3. However, the following rules must be followed:


Figure 4-1. Verification Test Pattern, Normal Indications


Warnime
THEMINAL COMTROLLER OPERATES ON llo vac power. ASSCCIATED POWER CONTROL PAMEL MAY have 220 10 240 VAC IMMUT.

## CAUTION

AWWAYS TURN TERMINAL CONTROLLES OFP gepore removing or Insertinc cheult cardos.

MAPPING MEMORY, LARCE MEMORY, and timing mooule contain mos OEVICES. WHEN MOT IN USE, STORE THESE CARDS IN STATICPROCF CONTAINERS.

NOTES

1. TECHNICAL MANUAL FOR DISPLAY MONITORS: TED
2. TECHNICAL MARUAL FOR MODEL 5783/5784 KEYBOAROS: Ho79-0363.
3. TECHNICAL MANUAL FOR MODEL 5781 PHOTOPEN: H-7BOOM2.
4. TECHNICAL MANUAL FOR MODEL STBG TRACKBALL/5787 FOREESTICX: 4078 m004. FOR MODEL $5 / 38$ DATA TABLET: H +81 -0159.
5. TECHNLCAL manual for Color harocopy unit: ounn moorl g31.
6. TECHNLCAL MANUAL FOR MONCCHROME HAROCOPY UNIT: TEKTRONIX MCDLL 4SZZ.
7. FOR SOFTWARE INSTRUCTIONS, SZE GRAPHIC Q PROGRAMMER'S REFERENCE

8. ALYONMENT PROCEDURE5:
thming module, paragraph am.
9. TECMNICAL MANLLAL FOR POWER SUPPLES:

MOOEL MAZ3-E0647/1'5
H-30-0087
10. if other models are used, approprlate manuals will be provided.
11. begore replacing a board, checx switch settings against the COMFIGURATION DOCUMENT.
12. The racxmane wiring for the standard caro cage is identical for card sLots iaixal thiough laixas, making the designated caro placement FOR THOSE SLOTS ARATRARY. EXCETT FOR THE READ NGIT: MEMORY CARDS: THE CANDS IN THESE EIGHT SLOTS CAN BE INTERCHANGED TO REASSIGN PROCESSOR bUS CONTROL PRORITIES AS DESIRED, WHTH THE BUS CONTRCL PRIORITY GRANT FINCTION BEING PASSED IN CAIE SLOT SEQUENCE FROM THE HIGHEST-PRIORITY SLOT (IAIXAI) TOWARD THE LOWEST-PRORITY CARO (GRAPHIC CONTROLLER IAIXA7). relocatable Caros must ze placed in ad hacent slots (ialkas,
 VACANT WCULD GREAK THE PRIORTY CHAIN, WHKH COULD RESULT IN UNIT malifunction. the readomrite memory cards are passive circuits that ARE ACCESSED BY THE PROCESSOR ZUS \$UT DO NOT SEIZE BUS CONTROL. THE grant signal is passed oirectly through a read write memory cano.

> Figure 4-3. Troubleshooting Flow
> Diagram (Sheet 1 of 4 )







Figure 4-3. Troubleshooting Flow
4-11/(4-12 blank)

1. All read/write memory cards should be installed in consecutive slots.
2. Those cards that can seize the processor bus should be installed in consecutive slots, to maintain the integrity of the grant bus. If there is a gap between such cards, you effectively get two grant buses in parallel, with subsequent interference when two cards want the bus at the same time. If it is necessary to separate such cards, you must connect the grant out connection (pin 35) of the higher priority card (lower numbered slot) across the gap to the grant in connection (pin 36) of the next lower priority card (higher numbered slot). For this type of connection, use Sanders part number 47067, daisy-chain jumper.

## ADJUSTMENTS

The timing module is the only card in the terminal controller requiring adjustment. The timing module is basically a phase-locked loop that locks to the line frequency or an external input. The line frequency may be 50 Hz or 60 Hz .

Any of several different timing modules may be used in a GRAPHIC 8 system, as follows:

| Non-interlaced | Interlaced |  |
| :---: | :---: | :--- |
| 5977168 Gl | 5977168 G 2 | Description |
|  | 5809995 Gl | Version 1 |
| 5810009 Gl | 5810010 Gl | Version 1.5 |
|  |  | Version 2 |

The following paragraphs give the instructions for adjusting the timing modules.
4.5.1 TIMING MODULE (PART NUMBER 5977168) ADJUSTMENTS. The timing module VCO frequency is approximately 45 MHz . Binary dividers reduce this frequency to VSYNC, which is phase-locked to the line frequency.

A light-emitting diode on the edge of the timing module blinks at a rate of 1.5 Hz , indicating that VSYNC is present and functioning correctly. If this indicator is either off or steady on, there is a problem with the phase-locked loop. Blinking at a rate slower than 1.5 Hz indicates improper lock, or that the line frequency is lower than 60 Hz .

Before adjusting the timing module, check that the configuration switches are properly set (refer to drawing 5977170 in H-81-0097).

Adjust the timing module as follows (see figure 4-4):

1. Set gain potentiometer R43 fully counterclockwise (maximum gain).
2. Open switch U30-S2.
3. Adjust offset potentiometer R42 for $-8 V$ at TP6.
4. Using oscilloscope, observe waveform VSYNC at J7. Sync internally and observe pulse output. The signal on the oscilloscope should have a pulse duration of 90 us at the line frequency. Adjust C 24 for the most stable display. (Pulse period is 16.6 ms for 60 Hz line, 20 ms for 50 Hz line.)
5. Change oscilloscope sync to line. Adjust C 24 to minimize movement of the signal on the oscilloscope.

6. Close switch U30-S2. The signal displayed on the oscilloscope should lock and be stable.
7. On second trace of oscillocope, observe signal at Tp6. This signal should be a triangular-shaped wave, centered around -8 V , with a peak-to-peak amplitude of approximately 1 V . The VSYNC waveform should track about midway on the rising edge of the triangle.
8. Adjust R42 as necessary to center VSYNC on the rising edge of the triangle. Leave R43 set for maximum gain.
9. Verify operation of all I/O signals against table 4-1.

Table 4-1. Timing Module Pin Assignments

| PIN/ TERM | SIGNAL NAME | DESCRIPTION |
| :---: | :---: | :---: |
| P1-1 | DB00 +H | Bidirectional data line, exclusively |
| P1-2 | $\mathrm{DBO}+\mathrm{H}$ | raster bus, TrIL levels, timing module |
| P1-3 | DBO2+H | output configuration switch settings to |
| P1-4 | DB03 + H | these lines for a short period after |
| P1-5 | DB04+H | TORN goes high. |
| P1-6 | DB05+H |  |
| P1-7 | DB06+H | Bidirectional data line, exclusively |
| P1-8 | DB07+H | raster bus, Tru levels, timing module |
| P1-9 | DB08+ H | output configuration switch settings to |
| P1-10 | DB09+H | these lines for a short period after |
| P1-11 | DB10+H | TORN goes high. |
| P1-12 | $\mathrm{DB} 11+\mathrm{H}$ |  |
| P1-43 | GCMR + | Timing module provides only a pull up |
| P1-44 | GRAS-H | to these bus signals. |
| P1-46 | GCAS-H |  |
| P1-48 | GRMK+H |  |
| P1-37 | EXTR- | TrL level input. When this signal is low, TORN-U will be low and a system reset will result. |
| P1-33 | 6.3 VAC | 6.3 volt RMS sine wave derived from the AC power source. |
| J1 | VIDEO 2 IN | Video input to a wideband variable gain amplifier. |
| J3 | VIDEO 1 IN | Video input to a wideband variable gain amplifier. |

Table $4-1$. Timing Module Pin Assignments (Cont)

| PIN/ TERM. | SIGNAL NAME | DESCRIPTION |
| :---: | :---: | :---: |
| P1-31 | $20 \mathrm{CK}+\mathrm{U}$ | TTL square wave $50 \%$ duty cycle $\pm 5 \%$ at 20 MHz . |
| P1-52 | RTCK-U | TIL square wave $50 \%$ duty cycle $\pm 5 \%$ at AC line rate. |
| PI-45 | TORN-U | THL pulse which is low for approximately 500 ms after power turn on. Signal then goes $h i g h$ and remains so until system power down or EXTR- active low input. |
| $\begin{aligned} & \mathrm{P} 1-27 \\ & \mathrm{P} 1-28 \end{aligned}$ | $\begin{aligned} & S C L K-U \\ & S C L R+U \end{aligned}$ | ECL square wave $50 \%$ duty cycle at pixel rate (balanced output). |
| $\begin{aligned} & \mathrm{Pl}-25 \\ & \mathrm{PI}-26 \end{aligned}$ | SHLD SHLD | Grounding point for SCLK-U and SCLKT-U bus strip shield. |
| P1-36 | CCLK-U | TYPL square wave $50 \%$ duty cycle at approximately 3.5 Hz . |
| P1-40 | BLNK - U | TTL square wave $50 \%$ duty cycle at 1.5 Hz. |
| P1-15 | ODDF+U | THL square wave $50 \%$ duty cycle at one half the vertical rate. |
| P1-47 | VAEE-U | TTL pulses active low for approximately 1250 usec at the vertical rate. |
| P1-49 | VAEN-U | TTL pulses active low for approximately 1250 us at the vertical rate. |
| P1-69 | VBLK-U | Tri pulses active low for approximately 1250 usec at the vertical rate. |
| P1-73 | HBLK-U | TTL pulses active low for 7 us at the horizontal rate. |
| P 1-70 | CSNC-U | TTL pulses derived from H SYNC, V-DRIVE, SERRATION, and VSYNC signals. |
| $J 5$ $J 6$ | CSYNC HSYNC | AC coupled TTL line driver output, polarity is determined by a switch setting. |

Table 4-1. Timing Module Pin Assignments (Cont)

| $\begin{aligned} & \text { PIN/ } \\ & \text { TERM. } \end{aligned}$ | SIGNAL NAME | DESCRI PTION |
| :---: | :---: | :---: |
| J7 | VSYNC | AC coupled TwL Line driver output, polarity is determined by a switch setting. Alternately VDRIVE can be routed to this output by a switch setting. |
| J 2 | VIDEO 2 OUT | Amplified video output. |
| J4 | VIDEO 1 OUT | Amplified video output. |

4.5.2 TIMING MODULE (PART NUMBER 5977168G2) ADJUSTMENTS. Proceed as for part no. 5977168G1, plus the following additional steps:

1. Connect the digital voltmeter between E8 and ground (see figure 4-5). Adjust R73 for a voltmeter indication of zero volts.
2. Wile observing the display, adjust $R 73$ for the clearest, most stable picture. There should be no break-up, rollover, horizontal movement, or fuzzy edges.
4.5.3 TIMING MODULE (PART NUMBERS 5810009 GI , 5810010 GI ) ADJUSTMENTS. Timing modules 5810009 Gl and 5810010 Gl have two adjustment procedures: one for the internal frequency loop, the other for external. synchronization.

### 4.5.3.1 Internal Frequency Loop Adjustment

1. Open switch U59-SI and close U59-S2 (see figure 4-6). Check that U59-S3 is closed.
2. Connect one channel of the oscilloscope to TP6, the other channel to TP5. Trigger on TP6. Adjust C31 (accessible through the right side of the can) until the two waveforms are stable at the proper period $(16.6 \mathrm{~ms}$ for 60 Hz line, 20.0 ms for 50 Hz line). Adjust C3l to minimize drift of the sawtooth waveform at $T P 5$. See figure $4-7$.
3. Now close U59-Sl and open U59-S2. While observing the signals at TP5 and TP6, carefully adjust C31 to align the pulse at TP6 with the zero crossing of the waveform at TPS.

### 4.5.3.2 External Synchronization Adjustment

1. Connect source signals to J 1 and J 3 . Set temination switches U33-SI and U33-S2 as needed (closed $=75$ ohms, open $=$ high impedance).


Eigure 4-5. Timing Module (Part No. 5977168G2) Adjustment Locations


Figure 4-6. Timing Module (Parts Nos. 5810009G1, 5810010GI) Component Locations

$4-92-46-932$

Figure 4-7. Timing Module Wave forms
2. Device U52 requires a positive input. Set U33-S3 and U33-S4 as required (when U33-S3 is closed and U33-S4 is open, an inverter is connected in the input line). Do not set both U33-S3 and U33-S4 closed at the same time.
3. Similarly set U33-S5 and U33-S6 as required to produce a positive external vertical drive signal. (When u33-S5 is closed and U33-S6 is open, an inverter is connected in the input line. Do not set both U33-S5 and U33-S6 closed at the same time.)
4. Open switch U59-S3. When you open this switch, the screen goes blank. Press the DIS/LOC pushbutton on the terminal controller front panel and wait for the verification test pattern to come up. If the pattern comes up garbled, adjust R25 throughout its range until the pattern becomes clear.
5. Connect the digital voltmeter to TP8 and adjust R17 for a voltmeter reading of +1.5 V , then adjust RI7 as required to center the image horizontally.
6. Retouch R25 as required for the best picture quality: no break-up, no roll-over, no fuzzy edges, and no hozizontal movement.

### 4.6 REPAIR

Repair consists of replacing circuit cards or chassis-mounted electrical assemblies suspected of being faulty, based on the troubleshooting analysis (figure 4-3).

## CAUTIOM

Always turn off terminal controller before removing or installing any circuit card.

Always turn off terminal controller and pull power plug before removing any chassis-mounted component.
4.6.1 CIRCUIT CARD REPLACEMENT. To remove a circuit card assembly, graps the two card extractor handles, exert outward pressure to disengage the card from its connector, and pull straight out of card cage.

Before installing a circuit card assembly, verify the part number of the card. Insert the card (component side to the left) into its slot in the card cage. Engage it with its connector by exerting firm inward pressure.
4.6.2 CHASSIS-MOUNTED COMPONENTS. Chassis-mounted assemblies are secured with standard mounting hardware, and can be removed and replaced using common hand tools.

Refer to the appropriate separate manual for maintenance of the teminal controller power supply.
4.6.3 SPECIAL HANDLING FOR MOS DEVICES. MOS devices are subject to damage caused by static charges. Assemblies that contain MOS devices are the read/write memory, mapping memories, and timing module. When not installed in the card cage, these assemblies should be stored in black Velostat bags with the MOS warning statement printed on the outside of the bag.

## CAUTION

Always handle these cards only by the card extractors or by the connector. Avoid touching the card components or the printed circuit.

### 4.7 PREVENTIVE MAINTENANCE

Preventive maintenance consists of visual and mechanical inspections and cleaning.
4.7.1 INSPECTIONS. Check that all power cords are in good condition, clean, and firmly plugged into their power outlets.

Check that signal cables are in good condition, clean, and that all connectors are tight.

Check that equipment control knobs are tight on their shafts.
Check that fans are operating in the card cage and power supply.
4.7.2 CLEANING. Clean equipment exteriors with a cloth dipped in warm soapy water and wrung out. Rinse with a cloth dipped in clear water and wrung out. Do not allow water to get inside any piece of equipment.

Clean monitor display surfaces with soft cloth and a commercial liquid glass cleaner. Never use an abrasive glass cleaner on the monitor display surfaces.

Inspect and clean air filter in the bottom of the equipment cabinet as follows:

1. Open rear cabinet door.
2. Loosen two screws on the air filter retainer (adjacent to the cable entryway). Move the filter retainer toward the cable entryway, then pull the air filter toward you one inch and lift out.
3. Clean the air filter by agitating it in warm soapy water; rinse it in clear water and dry it thoroughly with forced air before reinstalling in the equipment cabinet. Installation is the reverse of removal.

APPENDIX A

## RELATED PUBLICATIONS

The following listed publications apply to options that may be added to a GRAPHIC 8 system. The manual number listed is the manual's basic number. Revisions are indicated on the cover of the manual by a letter following this basic number.

| MANUAL NO. | TITLE |
| :---: | :---: |
| H-78-0042 | Model 5781 photopen Unit Technical Manual |
| H-78-0044 | Model 5786 Trackball/Model 5787 Forcestick Entry Devices Technical Manual |
| H-78-0408 | Large Read/Write Memory Circuit Card Assembly Technical Manual |
| H-82-0271 | Large Refresh Memory Part No. 5809944 Technical Manual |
| $\mathrm{H}-78-0435$ | Model 575 Hard Copy Multiplex Switch Technical Manual |
| $\mathrm{H}-79-0363$ | Model 5783 Alphanumeric Function Reyboard/Model 5784 Lighted Alphanumeric Function Keyboard |
| H-79-0450 | Model 7750 Expansion Module Technical Manual |
| H-81-0159 | Talos "Wedge" Data Tablet Operator's Manual (Talos Manual 50156-1 REV B) |

THEINTENTAND PURPOSE OFTHIS PUBLICATIONISTO PROVIDE ACCURATE AND MEANINGFUL INFORMATION TO SUPPORT EQUIPMENT MANUFACTURED BY CALCOMP/SANDERS. YOUR COMMENTS AND SUGGESTIONS AREREQUESTED.
PLEASE USE THE FORM ON THE REVERSE SIDE TO REPORT ANY PROBLEMS YOU HAVEHAD WITH THIS PUBLICATION OR THE EQUIPMENTIT DESCRIBES.


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[^0]:    *Separate power supply not required for runs up to 50 feet.

